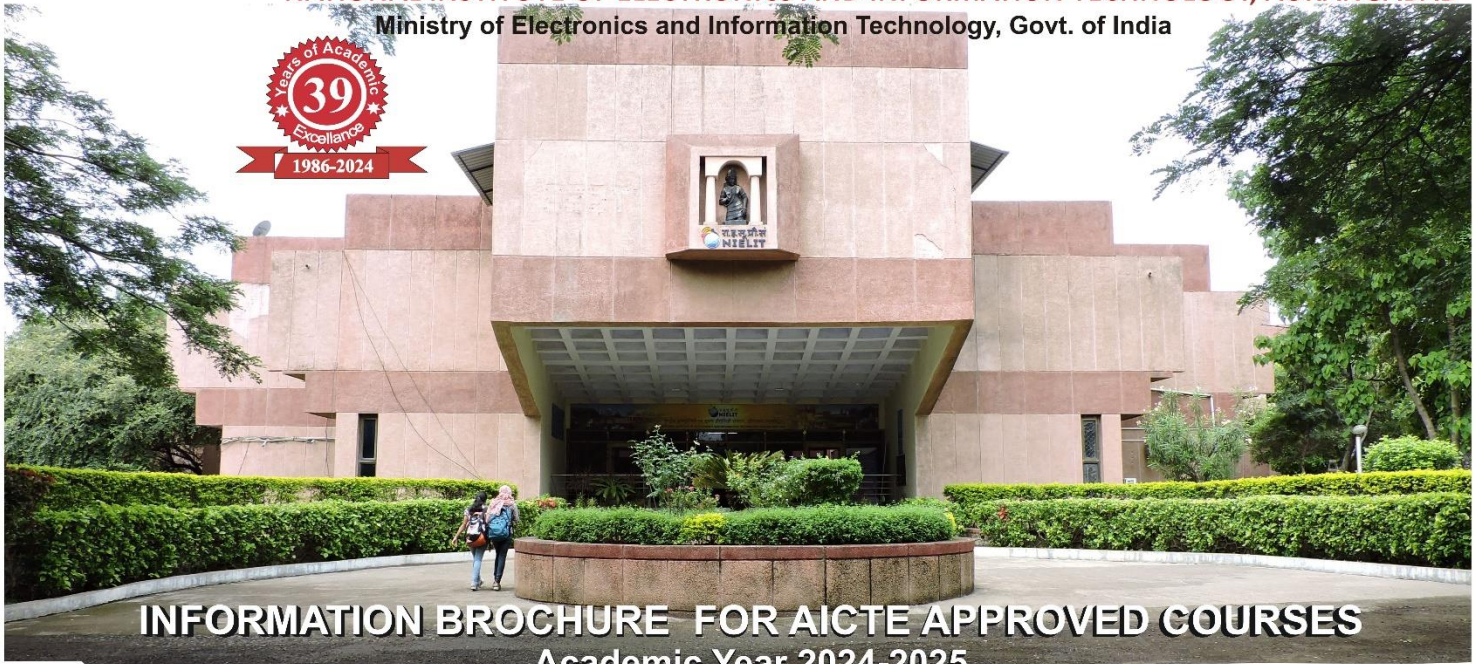




INFORMATION BROCHURE FOR AICTE APPROVED COURSES

Academic Year 2024-2025



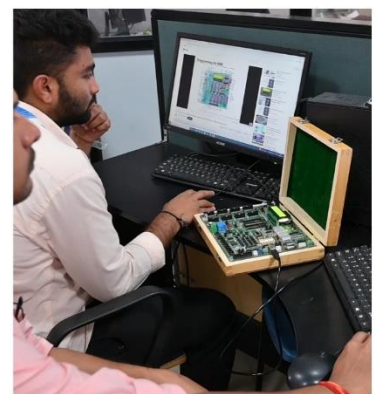
High Level of Excellence



Well Equipped Library & Labs



Industrial R&D Infrastructure



Students Project



Website: www.nielit.gov.in/aurangabad, Phone 0240 2982021,2982022

Message from Secretary Ministry of Electronics & Information Technology

The vision of the government to make India a USD 30-trillion developed economy by 2047 requires channelization of the demographic dividend and turning job-seekers into job-providers. Talent available is the biggest promise that can be realized by utilizing the opportunities around the range of emerging technologies. MeitY's approach aims to bring this about by building Digital Public Infrastructure rooted in inclusivity, accessibility, innovation and bridging of the Digital divide.

In this direction, National Institute of Electronics and Information Technology (NIELIT) has been conferred the esteemed status of "Deemed to be University" by the Department of Education, Government of India. This recognition reaffirms NIELIT's alignment with the strategic objectives outlined by the MeitY. This remarkable achievement also underscores NIELIT's unwavering commitment to excellence in the field of Information Technology and its dedication to providing world-class education and training.

NIELIT's elevation to deemed university status seeks to empower citizens, and drive socio-economic development with steadfast commitment to the principles outlined in the New Education Policy (NEP), which emphasizes holistic education, competency-based learning, and the integration of technology in education. As a deemed university, NIELIT will play a pivotal role in producing skilled professionals equipped to tackle the challenges of the digital age.

NIELIT Aurangabad, with its rich legacy and pioneering advancements in Information Technology, is well-positioned to contribute meaningfully to MeitY's vision. With its newfound status, NIELIT has the autonomy and flexibility to design and implement innovative programs that address the evolving needs of the industry and society, thus playing a crucial role in the development of India's Digital Public Infrastructure, skilled workforce required for attaining the goal of a Developed Nation.

I call upon all members of the NIELIT community to seize this historic opportunity and redouble our efforts towards excellence in education, research, and innovation. Let us work together to realize MeitY's vision of a digitally empowered India, where technology serves as an enabler of growth, inclusion, and prosperity.

Message from Director General



The education system in this country is undergoing a paradigm shift to improve upon the employability factor. With the launch of the 'Digital India' initiative by the Government of India, skill development and capacity building has assumed greater significance. This campaign is based on three fundamental pillars – to create digital architecture as a utility for citizens of India; to ensure digital delivery of services; and to digitally empower the citizens of India.

The National Institute of Electronics and Information Technology (NIELIT), a body under the administrative control of the Ministry of Electronics and Information Technology (MeitY), Government of India, is today uniquely positioned in terms of its outreach to all corners of the country and all segments of the society. NIELIT is a professional examination body in India, which also accredits training programs in IT and Electronics, specializing in the non-formal sector of education and training. It is actively engaged in the development of qualified human resources in the areas of IT, electronics, communication technologies, hardware, cyber law, cyber security, IPR, GIS, cloud computing, ESDM, e-Governance and related verticals. This gives the Institute a distinct identity and character in the panorama of Skill Development and Capacity Building in India. Many Government Departments have entrusted NIELIT for recruitment of technical manpower, as third-party recruitment not only ensures transparency but the technical expertise of NIELIT can be also used for online application and conduct of assessments for short listing of candidates.

NIELIT has made efforts to establish standards in the areas of IECT and has developed market oriented courses in the emerging areas as per needs of the IT and the electronic industry. Besides, NIELIT is also a preferred agency for rolling out capacity building initiatives of State Governments and Central Ministries.

Subsequent to the merger of erstwhile CEDTs (at Aizawl, Aurangabad, Calicut, Gorakhpur, Imphal, Jammu/ Srinagar and Guwahati/Tezpur) and RCCs (at Chandigarh and Kolkata) with DOEACC, this mega institution was rechristened as NIELIT in October, 2011. The merger had necessitated process re-engineering and standardization efforts so as to amalgamate the various units and promote best practices, as a merged single cohesive unit catering to the needs of stakeholders on PAN India basis.

With emphasis on a strong Online presence, NIELIT in the recent times has embraced technology to facilitate stakeholders, particularly students, through portals that provide end-to-end solutions, including Registration to Courses – e-Learning – Online Examinations – Digital Certificates, supported by automatic SMS, e-Mail and Aadhaar enabled Direct Benefit Transfer (DBT) scheme. The process is also being reinforced with 'digital locker' technology.

With 47 Centres all over India and a network of about 850+ Accredited Training Partners and about 7000+ Facilitation Centres (for Digital Literacy programs), this revamped website of NIELIT is a single source – device independent portal that provides for information and features in a comprehensive and cohesive manner, managed individually by all centres, under an 'umbrella' framework.

(Dr. Madan Mohan Tripathi)



Message from Executive Director

As the Executive Director of NIELIT Aurangabad Centre it gives me immense pleasure to share with you that this Centre is among the frontrunners and a leader in providing solution-oriented approach for promotion of Electronics and IT in the region. Located in the sprawling and lush green campus of the Dr. Babasaheb Ambedkar Marathwada University, the Centre is providing world-class educational and skill development opportunities to the youth through state-of-the-art teaching methodologies in both Formal and Non Formal sectors.

Established in 1987, the high-end courses provided by the Centre are aimed towards holistic development of students and the enviable success rate in term of employments boasts of many niche companies in both India and abroad.

With the motto of providing “Opportunities to All”, the NIELIT Aurangabad Centre is catering to a diversified range of courses starting from empowerment of local youth and underprivileged sections in the region through digital literacy programs to churning out of highly competent engineers through B.Tech and M.Tech programs. These programs are uniquely designed that allows students to advance their careers and creatively address multifaceted problems. Through affordable programs and excellence in education, which also includes industry aligned skill development programs, students are equipped with Intellectual and Professional skills based on ethical principles and international best practices.

The Formal courses offered by the Centre are approved by the All India Council of Technical Education (AICTE) and given the expertise that this Centre has developed over these years, Dr. BAM University has accorded NIELIT Aurangabad the prestigious status of a Research Centre, leading to the award of Ph.D. Degree in Engineering and Technology.

At NIELIT Aurangabad, we strongly believe that ‘Quality’ is not a destination, but a journey to improve. Thus, through a range of technical programs and courses that are aligned to the ‘Skill India’, ‘Make-In- India’ and the ‘Digital India’ initiatives of the Government of India, the Centre is endeavouring to lead by example in providing academic excellence and quality technical education to support the vision of the Hon’ble PM, Hon’ble Union Minister (E&IT) and Hon’ble Union MoS (E&IT) of a ‘सशक्त और समृद्ध भारत’. As the need for flexible, multidisciplinary, global education has increased, NIELIT Aurangabad Centre is also diversifying into the areas in the latest technological trends and is also expanding its reach to meet the industrial demands. The ideology of the Centre is based on the needs of modern engineering & technology education for modern age students supplemented with a vision & mission of a futuristic education system which is outcome oriented, transparent, accountable & accessible.

Being a key constituent unit of NIELIT, Ministry of Electronics and Information Technology (MeitY), Government of India, it also undertakes a wide range of Capacity Building programs aimed towards supporting the initiatives of the State Government in IT and related areas. It is, in fact, the preferred agency for rollout of Government initiatives on self-sustainable basis in areas ranging from skilling and empowerment of SC/ST and women; recruitment of technical manpower; R&D in IT and Electronics; Online services and many other technical related projects.

Under the dynamic and visionary leadership of Director General, NIELIT in the last few years has metamorphosed into a national institute of repute, scripting an extraordinary growth story, which can be rarely matched by other organizations. I encourage you all to be a part of this growth story and explore all that NIELIT Aurangabad Centre has to offer!

Dr. Jayaraj U Kidav
Executive Director

Governing Council of NIELIT

			
Shri Ashwini Vaishnaw Chairperson Hon'ble Minister of Communications and Electronics & Information Technology and Railways	Deputy Chairperson Hon'ble MoS Electronics & Information Technology	Shri S. Krishnan Executive Vice Chairperson Secretary, Ministry of Electronics and IT	Shri K. Sanjay Murthy Secretary, Department of Higher Education Ministry of Education
			
Prof. M. Jagadesh Kumar Member Chairman, UGC	Prof. T. G. Sitharam Member Chairman, AICTE	Ms. Trishaliit Sethi Member Director General (Training)/Additional Secretary, Ministry of Skill Development & Entrepreneurship	Sh. Bhuvnesh Kumar Member Additional Secretary [HRD] & Group Coordinator, MEITY
			
Shri Abhishek Singh Member Additional Secretary, MEITY	Shri Rajesh Singh Member Joint Secretary & Financial Adviser, MEITY	Mrs. Debjani Ghosh Member President, NASSCOM	Prof. Anil Kumar Saini Member President, IETE
			
Sh. T. V. Mohandas Pai, Member, Chairman, Manipal Global Education Services Pvt. Ltd.	Shri Hariom Rai Member Chairman, Lava International Limited	Shri Vineet Nayar Member, Founder Sampark Foundation	Dr. Madan Mohan Tripathi, Member Secretary Director General, NIELIT

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SAY NO TO RAGGING

According to UGC guidelines, the definition of ragging states that any conduct whether by words spoken or written or by an act which has the effect of teasing, treating or handling with rudeness any other student, indulging in rowdy or undisciplined activities which causes or is likely to cause annoyance, hardship or psychological harm or to raise fear or apprehension thereof in a fresher or a junior student or asking the student to do any act or perform something which such student will not in the ordinary course and which has the effect of causing or generating a sense of shame or embarrassment so as to adversely affect physique or psyche of a fresher or a junior student.

IMPORTANT INSTRUCTIONS FOR THE STUDENTS

1. As per the directions of the Hon'ble Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal number 887 of 2009, dated 08-05-2009, ragging is **strictly prohibited and banned**.
2. All students of the institute must complete and submit the affidavit online http://antiragging.in/site/affidavits_registration_form.aspx.
3. Ragging is a Cognizable Offence. Students are advised not to indulge in Ragging.
4. Ragging entails heavy fines and/or suspension/expulsion.
5. In case the applicant for admission to the institute is found to have indulged in ragging in the past or if it is noticed later that he has indulged in ragging, admission may be refused or he/she shall be expelled from the institution.
6. It is mandatory for the parents to report immediately to the Authorities of the Institute in case their wards inform them about ragging.

PUNISHABLE INGREDIENTS OF RAGGING

1. Abetment to ragging or Criminal conspiracy to rag
2. Unlawful assembly and rioting while ragging
3. Public nuisance created during ragging
4. Violation of decency and morals through ragging
5. Injury to body, causing hurt or grievous hurt
6. Wrongful restraint or Wrongful confinement
7. Use of criminal force
8. Assault as well as sexual offences or unnatural offences
9. Extortion or Criminal intimidation
10. Criminal trespass or Offences against property
11. Attempts to commit any or all of the above-mentioned offences against the victim(s)
12. Physical or psychological humiliation
13. All other offences following from the definition of "Ragging"

PUNISHMENT

Depending upon the nature and gravity of the offence as established by the Anti-Ragging Committee of the institution, the possible punishment for those found guilty of Ragging at the Institution level shaped any one or any combination of the following:

1. Suspension from attending classes and academic privileges
2. Withholding/ Withdrawing scholarship/ fellowship and other benefits
3. Debarring from appearing in any test/ examination or other evaluation process
4. Withholding results
5. Debarring from representing the institution in any regional, national or international meet, tournament, youth festival etc.
6. Suspension/expulsion from the hostel
7. Rustication from the institution for periods ranging from 1 to 4 semesters
8. Expulsion from the institution and consequent debarring from admission to any other institution for a specified period
9. Fine ranging between Rupees 25,000/- and Rupees 1 Lakh
10. Collective punishment: When the persons committing or abetting the crime of ragging are not identified, the institution shall resort to collective punishment
11. Freshers who do not report the incidents of ragging either as victims or as witnesses shall also be punished suitably.

As per the directions of the Hon'ble Supreme Court of India, if any incident of ragging comes to the notice of authority, the concerned student shall be given liberty to explain and if his/her explanation is not found satisfactory, the authority would expel him/her from the Institute"

1.0 NIELIT Aurangabad –An Introduction

1.1 Genesis

The history of NIELIT dates back to 1974 when the Department of Electronics (DoE), now the Ministry of Electronics and Information Technology (MeitY), Government of India, and the University Grants Commission (UGC) established the first **CEDT at the Indian Institute of Science (IISc)** premises in **Bangalore**, with assistance from the Swiss Development Corporation.

A decade after the successful establishment of CEDT Bangalore, the DoE (now MeitY) set up similar centers in **Aurangabad, Imphal, and Srinagar** in 1987, and in **Calicut, Mohali, and Gorakhpur** in 1989. The objective was to develop human resources at various levels and in specialized areas of Electronics Design, aiming to bridge the gap between academic institutions and industries.

In 2001, the CEDT centers in Aurangabad, Calicut, Gorakhpur, Imphal, and Srinagar were merged with **DOEACC**, a scientific society of MeitY. To mark its transformation into an Institute of National Importance, the society was rechristened as **National Institute of Electronics and Information Technology (NIELIT)** on October 10, 2011.

The **NIELIT Aurangabad** is co-located inside the lush green campus of Dr. B.A.M. University and its campus is spreads over **more than 18 acres**. It has about **14 well-equipped laboratories** and a **Mechanical workshop** besides a rich Library, Gymnasium for students, Auditorium, Canteen, Basket-ball ground, Volley ball ground, Kho Kho ground etc.

The Centre started offering unique AICTE approved courses viz **Diploma in Electronics Production and Maintenance** since 1987, **M.Tech (Electronics Design and Technology)** since 1990, **B.Tech (Electronic Engineering)** since 2013 and is also a Recognized Research Centre of the Dr. B.A.M. University, Aurangabad since 2007 for conducting research leading to award of Ph.D. Degree in Engineering and Technology.

Since 1987, the center has been offering unique AICTE-approved **Diploma** courses in **Electronics Production and Maintenance M.Tech in Electronics Design and Technology** and since 2013 **B.Tech. in Electronics Engineering** Additionally, since 2007, the Center has been a recognized Research Center of Dr. B.A.M. University, Aurangabad, conducting research leading to the award of Ph.D. degrees in Engineering and Technology.



The Centre also provides **consultancy** and other services to leading **Industries** of the region like Bajaj Auto Ltd, Videocon, Sterlite, Siemens, Meltron, Maharashtra Police Wireless, etc. It is also implementing

ESDM scheme sponsored by **Ministry of Electronics and Information Technology (MeitY)** for developing human resources with adequate competence levels in **Electronics Design & Production Technologies**.

The Industrial grade laboratories of the Centre are fully equipped with the latest systems and development tools in the area of Printed Circuit board, VLSI Design, Embedded Systems, Product Design, Digital Systems, Process Control & Instrumentation and CAD/CAM.

Besides numerous reference books, Journals, magazines; the students of the Centre have access to **MeitY Library Consortium** (good collection of latest e-Journals including IEEE and books) and National Knowledge Network (NKN) a strong a network with multi-gigabit capability connected to all universities, research institutions, libraries, laboratories, healthcare and agricultural institutions across the country.

All the labs, library and office are connected through the central network and students can retrieve information from their terminals ~~itself~~ and through well connected Wi-Fi system. The Centre organizes National Level Seminars/Workshops in areas like Agri- Electronics, Electronics Product Design, Intellectual Property Rights (IPR), Neural Networks, e-learning regularly.

Trained to become R&D engineers, students from the center are employed in **leading and reputed organizations** such as C.G Coral, Lucent India, Texas Instruments, L&T, HCL, Wipro Technologies, BITS, IIT, BEL, HAL, ISRO, DRDO, BARC, ECIL, Messung, Thermax, Honeywell, Cyrus Logic, and L&T EMSYS, among others.



The Centre has evolved into a solution-focused model organization and knowledge-based enterprise, dedicated to developing a pool of R&D engineers and entrepreneurs.

1.2 Objectives of Centre

- 1.2.1 To bring an **innovative, entrepreneurial spirit** along with excellence in teaching, learning and research to develop leaders in IT and Electronics.
- 1.2.2 To generate and keep updated Industry-ready quality professionals with knowledge-based skill set in IECT and allied fields through formal and informal education systems.
- 1.2.3 To establish a Quality system **of examination and certification** that is globally recognized and provides a fair assessment of the competency of students.
- 1.2.1 To maintain **close links** with **Industries, R&D and Academic Institutions** to promote Electronics, IT and industrial design culture.
- 1.2.2 To develop **entrepreneurs, experts and designers**, carry out R&D and provide **Industrial Consultancy** in IECT.
- 1.2.3 To offer **e-Training** in Electronics, Information Technology and Industrial Design methodology and production technique.

Mission: Identifying the needs of modern engineering & technology education and providing Quality Technical Education leading to Academic Excellence, creativity and innovation in the areas of Electronics and Information Technology.

Vision: To impart professional education that is outcome-oriented, combined with fostering innovative thinking, application of knowledge, inculcating professional ethics and consciousness to social responsibilities.

1.3 Product Design

The Centre offers world-class educational and skill development opportunities to the youth, with a course structure designed to foster a system-level understanding among students. Many of the B.Tech and M.Tech projects are sponsored by companies and yield hardware electronic products. Subsequently, some students leverage their expertise to launch commercial ventures.



Industry Interaction:

The institute offers a range of services to industries, including product design and development, product engineering, prototype development, process automation, and consultancy. Additionally, it is dedicated to enhancing its infrastructure to deliver high-quality services to industries, encompassing servicing and maintenance of sophisticated instruments and machines, support in technology absorption, and procurement of the latest equipment and machines.

1.4 R & D, Projects and Consultancy

Post Graduate level academic projects are of one (01) year duration, whereas Diploma and B.Tech level projects are of one (01) semester (six months) duration. Students are encouraged to interact with industry to expose themselves to industry environment and get motivated to undertake real problems of industry as their innovative project work, guided by the faculty. In addition to the above, the institute also undertakes Government as well as industry-sponsored projects. Some of them are “Training of Teachers in e-learning”, “Information Security Education & Awareness” and Women Empowerment through Value Added Skill Development in IECT”. Apart from the above, consultancy is also provided to the industry. The ongoing projects at NIELIT Aurangabad are as follows:

1. Chips to Start-up Programme
2. ISEA-Phase-III

3. Electronics and ICT-Phase-II
4. Employability Enhancement Livelihood Training Programme (EELTP)
5. DRONE UAS Capacity building Skill Development of SC / ST / EWS youths in Aspirational District in the area of IECT leading to enhancement in Employability
6. Work Based Learning
7. Future Skill Prime-RPA(Lead)
8. Future Skill Prime-3D Printing and AM (Co-Lead)
9. Future Skill Prime-Social and Mobile (Co-Lead)
10. Future Skill Prime-IoT (Co-Lead)

1.5 State-of-the-Art Laboratories

- i. CAD/CAM
- ii. Consumer Electronics
- iii. Industrial Automation
- iv. Internet of Things
- v. Network & Server Facilities
- vi. Opto-Electronics
- vii. Power Electronics
- viii. Printed Circuit Board
- ix. VLSI Design
- x. Embedded System Design
- xi. Open-Source Computing
- xii. AR/VR Lab
- xiii. Multimedia Lab
- xiv. Additive Manufacturing/3D Printing Lab
- xv. Robotics Process Automation (RPA) Lab
- xvi. Central Workshop



1.6 Other Amenities /Facilities:

Table No. 01

a. Lecture Halls	l. Uninterrupted Power (63KVA DG Set)
b. Seminar Hall	m. Cafeteria
c. Conference Hall	n. Boy's Hostel
d. Auditorium	o. PG Boy's Hostel
e. Local Area Network with 225 (100 Mbps) Nodes.	p. Warden Quarters
f. Leased line internet connectivity	q. Guest House
g. Library with online access to IEEE Journals and National Digital Library of India along with a rich print collection of books, journals and magazines (<i>refer Annexure XXVIII</i>)	r. Vehicle Parking
h. Virtual Smart Class-Room facility	s. Open Theatre
i. Placement Cell and Model Career Centre	t. Record Room (143 Sqm)
j. Gymnasiums(Separate for Boys & Girls)	u. Sports Facilities
k. Dramatics, dance and Extra-Curricular	v. Jogging Track

1.7 Student Life

The coursework is project-based, allowing students ample time for innovation. Various sports and cultural clubs

on campus, managed by the student community, offer a range of extracurricular activities.

- i. Cricket
- ii. Badminton
- iii. Lawn Tennis
- iv. Basket-Ball
- v. Body Building
- vi. Drama Club
- vii. Music Club
- viii. Athletics
- ix. Literary and Fine Arts
- x. Photography
- xi. Debate Competition and Sports week

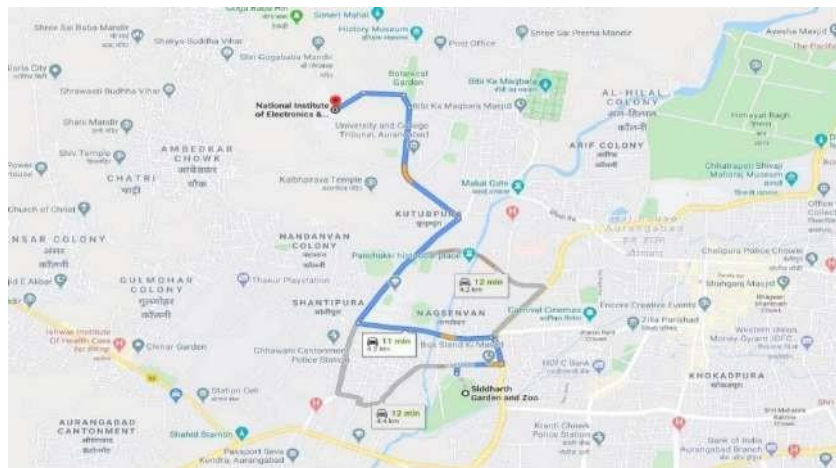








1.8 Location



National Institute of Electronics and Information Technology Aurangabad
Dr. Babasaheb Ambedkar Marathwada University Campus,
Aurangabad, Maharashtra-431004

Website: <http://nielit.gov.in/aurangabad/>

Landline: (+91-240) 2982021, 2982022

Fax: (+91-240) 2982050

2.0 Formal Courses

The Institute is offering the following AICTE-approved courses:

- Diploma in Electronics Production and Maintenance (DEPM)
- B. Tech in Electronic Engineering.
- M. Tech in Electronics Design and Technology
- Part-time M. Tech in Electronics Design and Technology

These courses are practical oriented and are designed with an emphasis on design and project work. The quality of education is maintained by periodic review and update of syllabus considering the latest trends and needs of industry, in-depth study by the students through semester system, transparent evaluation system and flexibility being autonomy granted to the Centre by Dr. B.A.M. University, Aurangabad(M.S). The Centre enjoys the reputation of its students getting employed in reputed industries and organizations almost immediately on their completion of courses or settled as successful entrepreneur.

2.1. Important Dates

Table No. 02

Sr. No.	Last date	DEPM First Year and Lateral entry	M.Sc in CS with DS	B. Tech (Regular Entry)	B. Tech (Lateral Entry)	M. Tech (EDT) Full-time	M. Tech (EDT) Part-time	Certified Programmes
1.	Downloading Application form from website	Please visit website @ www.dtemaharashtra.gov.in	Please visit website https://nielitaurangabad.in/admission	Please visit website http://josaa.nic.in/ and http://csab.nic.in/ https://nielitaurangabad.in/admission	15 th May, 2024	Please visit website https://ccmt.admissions.nic.in https://nielitaurangabad.in/admission	14 th June, 2024	https://nielit.gov.in/aurangabad/index.php
2.	Receipt of Application Form along with Fees				30 th June, 2024		10 th July, 2024	
	Written Test (tentative)				27 th July, 2024		13 th July, 2024	
4.	Declaration of list of selected & waitlisted Candidates				28 th July, 2024		15 th July, 2024	
5.	Document Verification and Admission to the course				29 th July, 2024		15 th July, 2024	
6.	Waitlist student Admission				29 th July, 2024		17 th July, 2024	
7.	Academic calendar starts				31 st July, 2024		1 st Aug. 2024	

Note:

- All dates provided are tentative, and any changes will be communicated exclusively through the website.
- The list of **Selected & Waitlisted Candidates** will be displayed on the website only.
- The dates for admission in Diploma course will be as per the schedule of **CAP 2024**.
- The dates for admission in B. Tech course will be as per the schedule of **JoSSA/CSAB 2024**.
- The dates for admission in M. Tech course will be as per the schedule of **CCMT 2024**.
- The Competent Authority reserves the right to **extend** the **Last Date** at his discretion.
- Spot admission (B.Tech & M.Tech)** is granted against available vacancies only, applying in advance does not guarantee admission.

Important Links

Table No. 03

Sl. No	Purpose	URL
1.	Downloading of Application Form	https://www.nielit.gov.in/aurangabad https://nielitaurangabad.in/admission
2.	List of Selected & Waitlisted Candidates	
3.	Date and time for operation of the waiting list	

2.2. Process for Payment for Fees

Table No. 04

Name of the Institute/Beneficiary	National Institute of Electronics and Information Technology (NIELIT)
Name of the Bank	State Bank of India
Branch	Samarth Nagar Aurangabad Maharashtra
Saving Bank Account Number	32078399585
IFSC/RTGS NO	SBIN 0007919
Mode of Electronic Transfer	NEFT, SBICollect, Website: www.onlinesbi.com

2.3. Syllabus of Written Test

Table No. 05

Sl. No	Purpose	Syllabus
1.	B.Tech (Lateral Entry)	Diploma (Electronics) syllabus recognized by Maharashtra State Board of Technical Education
2.	M.Tech (EDT) for part time	Gate 2024 Syllabus for (Common Subject of Electronics and Communication/ Instrumentation Engg./ Electrical Engg)

2.4. Written Test for Admission

1. The written entrance test will be of 1½ hours duration
2. The questions will be of objective type, wherein the candidate is provided, multiple choice questions
3. The candidate is required to mark the correct answer in the same sheet, provided to him/her.
4. The candidates are required to bring Pen, HB Pencil, Sharpener and Eraser.
5. Candidates are not allowed to take the Question/Answer booklets outside the exam hall.
6. Test will be in English Medium only.
7. Calculators, Mobile, Digital Diary, Logbooks and Pocket PCs **are not allowed in the Exam Hall.**
8. **Conduction of written test is subjected to the number of candidates applied for the course.**

2.5. Important Information

1. The student is required to submit the undertaking to agree to abide by the terms and conditions of the institute and AICTE New Delhi at the time of admission in the prescribed format as per Annexure IV(B)&(C) and counter signed by parent / guardian.
2. The students are encouraged to interact with industry for getting familiar to industry environment and to study & undertake real problems being faced by them as their project work.
3. All the students (boarders as well as day scholars) are required to strictly abide by the rules of the Institute / Centre, failing which disciplinary action may be taken against them.
4. The mess is attached to the hostel.
5. Mess–Canteen facility is compulsory for the students who reside in NIELIT Hostel.



3. Diploma in Electronics Production & Maintenance (DEPM)

This is a three-year (six-semester) course that prepares students for careers as Production/Maintenance Supervisors, Design Assistants in the electronics or allied industries, or as entrepreneurs. The course is approved by AICTE, New Delhi, and the Maharashtra State Board of Technical Education (MSBTE), Maharashtra (India). Admission to the first year of the DEPM program and DEPM lateral entry is through the Centralized Admission Process (CAP) conducted by the Directorate of Technical Education (DTE), Maharashtra.

3.1. Admission in 1st Year and Lateral Entry/Direct Admission in 2nd Year

3.1.1. The minimum eligibility criteria for both First Year and Lateral Entry/Direct Second Year admissions are determined by the Centralized Admission Process (CAP) conducted by the Directorate of Technical Education (DTE), Maharashtra. Please visit www.dtemaharashtra.gov.in for the minimum eligibility criteria and seat matrix.

3.1.2 Seat Matrix

Total 60* seats are available in DEPM. Please check www.dtemaharashtra.gov.in for seat matrix.

***Number of seats indicated above are tentative and subject to be change by Government orders given time to time.**

Important

- i. Seats are reserved for candidates belonging to reserved categories as indicated in the Centralized Admission Process (CAP) conducted by the Directorate of Technical Education (DTE), Maharashtra.
- ii. Seats for General, OBC, SC, ST, PWD, and Foreign National candidates are reserved as detailed on www.dtemaharashtra.gov.in.
- iii. Seats for Foreign Nationals are reserved as described by the Directorate of Technical Education (DTE), Maharashtra.
- iv. Admission of the selected candidates is subject to verification of documents and payment of applicable fees.
- v. Candidates selected in the CAP round of DTE Maharashtra are required to register on the notified day, submit the required documents, and pay the admission fees as mentioned in Section 12.0; otherwise, their claim may be forfeited.
- vi. The admission process for the DEPM course is completed when the approved intake of candidates as per the seat matrix is provisionally admitted and registered, or when the time limit decided by the competent authority is over, whichever is earlier.

3.2 Academic Calendar – Refer ANNEXURE –I

3.3 Scheme of Instruction for First Year and Direct Second Year:

- 3.3.1 Every student must register for all subjects of a semester as outlined below. A mandatory six-week In-Plant Industrial Training is required for DEPM students during the summer vacation at the end of the IV Semester.
- 3.3.2 Detailed curriculum may be downloaded from <https://www.nielit.gov.in/aurangabad>.

3.4 Term Course Load:

In each semester, subject load varies from 33 to 41 credits per semester.

3.5 Assessment:

- 3.5.1 A student is eligible to take the semester exam if they meet the following requirements: Minimum 75% attendance in both theory and practical classes, completion of all sessional assignments, and cleared all dues.
- 3.5.2 Non-appearance in any examination is treated as an automatic zero mark in that subject. This policy applies to both theory and practical examinations.
- 3.5.3 The evaluation is based on an average weightage system. Every subject has credit points based on the hours of study required.
- 3.5.4 Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.
- 3.5.5 Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.
- 3.5.6 Every student is awarded Grade points out of maximum 10 points in each subject. (Based on 10 Points Scale).
- 3.5.7 Based on the Grade points obtained in each subject, Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed as per **ANNEXURE-II**.

3.6 Award of Diploma:

- 3.6.1 A student must complete the minimum credit requirements within a maximum period of six (6) years and obtain a minimum CGPA of 4.0 in the course to qualify for the award of the Diploma.
- 3.6.2 The Diploma is awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad.
- 3.6.3 It is also recognized by the Directorate of Technical Education, Government of Maharashtra. It has been granted equivalence by MSBTE, stating that the Diploma in Electronics Production and Maintenance is equivalent to the Diploma in Electronics Engineering as per the MSBTE curriculum.

3.7 Scholarship

The Institute encourages and assists all students, particularly those from reserved categories, in applying for Central/State Government scholarships for tuition fee reimbursement and maintenance allowance as per State Government rules, where applicable.

3.8 Assistance in Placement / Pursuing Higher Studies:

The Centre's Placement Cell provides comprehensive assistance to students seeking employment or self-employment opportunities. Nearly all graduating students from the Centre secure promising career opportunities.

4.0 B.Tech. in Electronic Engineering:

- 4.1 This course covers a spectrum of topics directly applicable to industry, preparing students for careers in electronics product development. It encourages research in practical applications within this rapidly evolving field. The curriculum prioritizes innovation, design, and the development of electronic products. NIELIT Aurangabad currently offers a successful B.Tech program in

Electronics Engineering, and starting from the 2024-2025 academic session, a B.Tech program in Computer Science Engineering will be introduced to meet student demand.

4.2 Eligibility for Admission in 1st Year

Admission to B.Tech. (Electronic Engineering) being offered by NIELIT Aurangabad will be based on JEE (Main) conducted by the National Testing Academy (NTA) on behalf of the Govt. of India. The Joint Seat Allocation(JoSAA) / Central Seat Allocation Board (CSAB) will conduct centralized admissions for this course. For further details candidates are advised to **please visit** <http://josaa.nic.in/> and <http://csab.nic.in/>

About Joint Seat Allocation Authority (JoSAA)

The **Joint Seat Allocation Authority (JoSAA)** has been set up by the Ministry of Human Resources Development (MHRD) to manage and regulate the joint seat allocation for admissions to IITs, ISM, NITs, IIITs and Other-Government Funded Technical Institutes (Other-GFTIs). Admission to all the academic programs offered by these Institutes will be made through a single platform.

- Eligible candidates must register and select their preferred academic programs through JoSAA.
- Document verification is required for candidates selected by JoSAA to be accepted and admitted to the Institute.
- The Central Seat Allocation Board conducts a special vacant seat filling round (Special round). For further details, please visit: <http://csab.nic.in>.
- Candidates allotted to NIELIT Aurangabad must report to the Institute with all original certificates and a photocopy of each document verified at the reporting centre within the specified timeframe.
- Candidates must deposit any outstanding fees (if applicable) through NEFT/RTGS

4.3 Direct Admission:

Admission to the B.Tech first year is conducted directly against available vacancies, solely based on merit. Applying in advance does not guarantee admission. Admission is granted exclusively based on merit and in accordance with the available vacancies after JoSAA rounds as mentioned above. For more details please visit:

<https://www.nielit.gov.in/aurangabad>
<https://nielitaurangabad.in/admission>

4.3.1. Seat Matrix

Total 60* seats are available in B.Tech. Please visit <http://josaa.nic.in/> and <http://csab.nic.in/> for category wise seat matrix.

** Number of seats indicated above are tentative and subjected to be change by Government orders given time to time. There is a separate quota of 9 seats for Student studying abroad. Interested candidates will have to take admission through DASA <https://www.dasanit.org/>*

4.4 Lateral Entry (Direct Second Year) in B.Tech. Electronic Engineering

4.4.1 Minimum Eligibility Criteria

Three Year Diploma in Electronics & allied streams with **minimum 45% marks** (40 percent for SC/ST candidates) are eligible for **Lateral Entry (Direct Second Year) in B.Tech.**

4.4.2 Seat Matrix: Vacant Seats (if any)

Note: General, OBC, SC, ST and PWD are reserved as per Govt. of India Rules, AICTE and/or University Approval.

4.4.3 Selection Process for Lateral Entry

- 4.4.3.1 Only the Candidates meeting the minimum eligibility criteria will be eligible for admission.
- 4.4.3.2 Admission to lateral entry (direct second year) in B. Tech will be on the basis of written exam conducted by NIELIT Aurangabad.
- 4.4.3.3 The eligible candidates have to download the Application Form and has to submit the same on email id : **btech-abad@nielit.gov.in**
- 4.4.3.4 The Application fees will be Rs.1000/- non-refundable. However, the candidates belonging to SC/ST/PWD are exempted from application fees.
- 4.4.3.5 The Merit List for admission to **2nd Year B. Tech (Lateral Entry)** will be based on the score in the Written Test and/or score in Diploma Electrical/Electronics and allied streams.
- 4.4.3.6 Admission of the Selected Candidates will be subject to their verification of Documents and payment of applicable fees.
- 4.4.3.7 The category-wise Main List (selected) and Waiting List of the candidates for admission to (year 2024-25) of **2nd Year B. Tech (Lateral Entry)** will be displayed **only on the website and Notice Board of the Institute.**
- 4.4.3.8 The date and time for counselling of the waiting list shall also be declared along with the list of selected candidates. All the waitlisted candidates should make themselves available at the time of the waiting list counselling, otherwise their claim shall be forfeited.
- 4.4.3.9 The waitlisted candidates, available at the time of the waiting list counselling, shall be provisionally admitted as per the merit of the category-wise waiting list.
- 4.4.3.10 The selected candidate from main list and waiting list candidates are required to register on the day as notified along with the documents and by paying fees for admission as mentioned in Section **12.0, otherwise their claim can be forfeited.**

4.5 Admission in the event of cancellation of secured admission

If any vacancy arises after the completion of the admission process, the vacancy may be filled on case to case basis at the discretion of the Competent Authority as per below mentioned procedure:

- i. Preference shall be given as per the ranking in the common merit list.
- ii. The Selected Students as per ranking in the Merit List, who could not reach the Centre for admission on the pre-intimated day because of legal and/or genuine reason(s) and approaching/contacting the Institute are first considered for filling the said vacancy.
- iii. After (i) & (ii), the candidates, who have not been offered admission and are approaching, may be considered.

4.6 Academic Calendar – Refer ANNEXURE –I

4.7 Scheme of Instruction for First Year (2024-2025):

- i. Each student is required to register for all subjects listed for a semester as outlined below.
- ii. For a detailed curriculum, please visit <https://www.nielit.gov.in/aurangabad> to download the necessary documents.

4.8 Term Course Load:

In each semester, there are 20 credits.

4.9 Assessment:

- 4.9.1 A student is eligible to take the semester exam if they meet the following requirements: - Minimum 75% attendance in both theory and practical classes, completion of all sessional assignments, and cleared all dues.
- 4.9.2 Non-appearance in any examination is treated as an automatic zero mark in that subject. This policy applies to both theory and practical examinations.
- 4.9.3 The evaluation is based on an average weightage system. Every subject has credit points based on the hours of study required.
- 4.9.4 Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.
- 4.9.5 Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.
- 4.9.6 Every student is awarded Grade points out of maximum 10 points in each subject. (Based on 10 Points Scale).
- 4.9.7 Based on the Grade points obtained in each subject, Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed as per ANNEXURE-II.
- Note:** For computation of SGPA CGPA, refer **ANNEXURE- II**.

4.10 Award of Degree:

A student must complete the minimum requirement of credits in **maximum period of eight(8) years** and must obtain a **minimum CGPA of 4.0** in the course to qualify for award of Degree. The Degree is awarded by Dr. Babasaheb Ambedkar Marathwada University (BAMU), Aurangabad. Starting from the academic session 2024-25, degrees are proposed to be awarded by NIELIT Deemed to be University, pending approval. Students will have the option to select the awarding university (i.e., BAMU or NIELIT Deemed to be University).

4.11 Scholarship

The Institute encourages and provides assistance to all the students, particularly the reserved category, to apply for the Central / State Govt. scholarship for reimbursement of tuition fee and maintenance allowance as per State Govt. rules.

4.12 Assistance in Placement / Pursuing Higher Studies:

The Centre's Placement Cell provides comprehensive assistance to students seeking employment or self-employment opportunities. Nearly all graduating students from the Centre secure promising career opportunities.

5.0 MASTER OF TECHNOLOGY (ELECTRONICS DESIGN AND TECHNOLOGY)[M. Tech (EDT)] Full Time Course

This is four semesters (2 years) AICTE approved Postgraduate course affiliated to Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS). Starting from the academic session 2024-25, degrees are proposed to be awarded by NIELIT Deemed to be University, pending approval. Students will have the option to select the awarding university (i.e., BAMU or NIELIT Deemed to be University).

5.1 Eligibility/ Selection Procedure

Admission to M. Tech. degree courses will be through Centralized Counselling or M.Tech (CCMT) 2024. For details, please visit <https://ccmt.admissions.nic.in/>

5.2 Intake (28 Seats)

Two seats are reserved for Industry Sponsored Candidates and **three seats** are reserved for Non-Resident Indian (NRI) / Persons of Indian Origin (PIO) / Children of Indian workers in the Gulf Countries (CIWG) quota. Distribution of seats is as under:

Table no. 06

Seat Matrix	
Open	9

Open- PWD	1
EWS	2
OBC	6
SC	3
ST	2
Industry Sponsored	2
Foreign Nationals	3
Total	28

Important

- Seats are reserved as per Govt. of India Rules, AICTE and/or University Approval.
- A quota of 15 % is reserved for the SC candidates, 7.5% for ST candidates, and 27% for Other Backward Classes.
- Candidates selected against the quota for persons with disabilities (5%) as per PWD Act 1995 are placed in the appropriate category viz.SC/ST/OBC/ Open candidates depending upon the category to which they belong in the roster meant for reservation of SCs/STs/OBCs.

5.3 Selection Process for Admission in 1st year M. Tech (EDT) Fulltime

A) National Applicants

- For Admission, please visit <https://ccmt.admissions.nic.in/>
- Vacant seats after all rounds of CCMT 2024 will be filled in on the basis of receipt (firstcome first serve basis) of application form (ANNEXURE VI). However, in case of anydiscrepancy with respect to vacant seats, the decision of Executive Director, NIELIT Aurangabad will be final and binding to all.

B) International Applicants

- Admission of Foreign Nationals is subject to guidelines, laid down by Government of India from time to time.
- Persons of Indian Origin (PIO) is an individual with foreign citizenship, except Pakistanand Bangladesh, without “NRI” status, holding a Foreign Passport at the time of applying for admission as well as during the study period and is himself/herself or anyone/both of his/her parents or anyone/both of his/her grandparents is/was/were Indian citizens.
- Children of Indian workers in the Gulf Countries (CIWG) are children of an Indian whois working in Gulf Countries under relevant working visa.
- Non-Resident Indian (NRI) Candidate is Child/ward of the person having ‘NRI status’ as defined under section 6 of the Income Tax Act.
- Foreign nationals may apply for admission to M. Tech (EDT) Full time course subject to fulfilling the minimum eligibility requirements through proper channel.
- Their application, will however, be considered separately on receipt of application, mentioned in ANNEXURE-IV (A) on first cum first serve basis.
- Foreign nationals are required to download and submit the application form for eligibility cum admission ([ANNEXURE-IV\(A\)](#)) and declaration & undertaking format([ANNEXURE-IV\(B\)](#)) along with payment of Rs.5000/- or equivalent USD (non-refundable).

5.4 Academic Calendar – Refer ANNEXURE –I

5.5 Scheme of Instruction for First Year (2024-25):

- Every student has to register for all the subjects of a Semester as mentioned below.
- Detailed curriculum may be downloaded from <https://www.nielit.gov.in/aurangabadindex.php>.

5.6 Term Course Load:

- In each semester, subject load varies from 11 to 21 credits per semester.

5.7 Assessment:

- i. A student is permitted to appear for the semester examination subject to he or she has a minimum attendance of 75% in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues.
- ii. Non-appearance in any examination is treated as the student having secured zero mark in that subject end semester examination.
- iii. The evaluation is based on an average weightage system. Every subject has credit points based on the hours of study required.
- iv. Every student is awarded Grade points out of maximum 10 points in each subject. (Based on 10 Points Scale).
- v. Based on the Grade points obtained in each subject, the Semester Grade Point Average (SGPA) and then the Cumulative Grade Point Average (CGPA) are computed as per **ANNEXURE-II**.

5.8 Award of PG Degree:

A student must complete the minimum requirement of credits in **maximum period of four (04) years** as per M.Tech Ordinances and Regulations in order to qualify for award of Degree. The PG Degree is awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS). Starting from the academic session 2024-25, degrees are proposed to be awarded by NIELIT Deemed to be University, pending approval. Students will have the option to select the awarding university (i.e., BAMU or NIELIT Deemed to be University).

5.9 Scholarship

- i. Non-sponsored students, admitted to the M Tech (EDT) course, provided they have a valid GATE SCORE, are eligible for the scholarship, subject to sanction and receipt of the said amount from AICTE New Delhi. Disbursement of PG-Scholarships is through direct cash transfer scheme through AICTE portal subject to his/her applying for the same and fulfilling the conditions. The award of scholarship and its continuation is subject to regular attendance, satisfactory progress, good conduct and abiding by the rules of the Institute.
- ii. Sponsored students, or students not having a valid GATE SCORE are not entitled for the said scholarship.
- iii. The tuition fee is exempted for SC/ST candidates only, subject to his/her applying for the same and fulfilling the conditions.
- iv. It is obligatory for every student, granted admission to M. Tech (EDT) and awarded scholarship, to undertake work related to teaching and research activities as assigned to him/her.

5.10 Assistance in Placement / Pursuing Higher Studies:

The Placement Cell of the Centre offers all assistance to the students for employment / self-employment. Almost all the students passing out from the Centre are getting good career opportunities.

6.0 MASTER OF TECHNOLOGY (ELECTRONICS DESIGN AND TECHNOLOGY)[M. Tech (EDT)] PART TIME COURSE

This is six semesters (3 years) AICTE approved course for working professionals with the Degree awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS). Starting from the academic session 2024-25, degrees are proposed to be awarded by NIELIT Deemed to be University, pending approval. Students will have the option to select the awarding university (i.e., BAMU or NIELIT Deemed to be University).

6.1 Eligibility

- i. B.E./ B. Tech Degree or equivalent in Electronics/ Electrical/ Telecommunication/ Instrumentation engineering etc. from a recognized University with at least 55% of marks.
- ii. The candidate should have been serving in Academic Institution/ Industry/ R&D organization

engaged in electronic product or system development for at least two years after completion of B.E./ B. Tech degree

- iii. The candidate should be sponsored by the employer.
- iv. His/ Her working place should be within 60 km distance from the institute.
- v. He should produce necessary sponsorship certificate along with application in the prescribed form given in ANNEXURE XII.

6.2 Intake: 24 Seats

6.3 Selection Process

- i. Only the Candidates meeting the eligibility criteria will be eligible for admission.
- ii. The eligible candidates have to fill the Application Form (ANNEXURE VI) and has to submit the same on **email id: mtech-abad@nielit.gov.in**
- iii. The selection for admission to M.Tech (EDT) part Time Course, will be based on **score in the written test**. List of Shortlisted Candidates will be displayed on the institute website.
- iv. Admission of the Selected Candidates will be subject to their verification of Documents and payment of applicable fees.
- v. The date and time for the waiting list counselling shall also be declared along with the list of selected candidates. All the waitlisted candidates should make themselves available at the time of the waiting list counselling, otherwise their claim shall be forfeited.
- vi. The waitlisted candidates, available at the time of counselling of the waiting list, shall be provisionally admitted as per the merit of the category-wise waiting list.
- vii. The selected merit and waiting list candidates are required to register on the day as notified along with the document and by paying fees for admission as mentioned in **Section 10.0, otherwise their claim can be forfeited**.
- viii. The decision of Executive Director, NIELIT, Aurangabad in respect of selection and closing of admission will be final.

6.4 Admission in the event of cancellation of secured admission

If any vacancy arises due to leaving the course by a registered student after completion of admission process, the vacancy may be filled on case to case basis at the discretion of the Executive Director, as per the procedure given below:

- i. Preference shall be given as per the ranking in common merit list.
- ii. The Selected Students as per ranking in Merit List, who could not reach the Centre for admission on the pre-intimated day because of legal and/or genuine reason(s) and approaching/contacting the institute are first considered for filling the said vacancy.
- iii. After (i) & (ii), the candidates, who have not been offered the admission and approaching, may be considered.

6.5 Academic Calendar – Refer ANNEXURE –I

6.6 Scheme of Instruction:

- i. Every student has to register for all the subjects of a Semester as mentioned below.
- ii. Detailed curriculum may be downloaded from <https://www.nielit.gov.in/aurangabad>.

6.7 Term Course Load:

- i. In each semester, subject load varies from **10 to 20 credits** per semester.

6.8 Assessment:

- i. A student is permitted to appear for the semester examination subject to he or she has a minimum attendance of 75% in theory and practical classes, completes all his/her sessional assignments and

clears all his/her dues.

- ii. Non-appearance in any examination is treated as the student having secured zero mark in that subject and semester examination.
- iii. The evaluation is based on an average weightage system. Every subject has credit points based on the hours of study required.
- iv. Every student is awarded Grade points out of maximum 10 points in each subject. (Based on 10 Points Scale).
- v. Based on the Grade points obtained in each subject, Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed as per **ANNEXURE-II**.

6.9 Award of PG Degree:

A student must complete the minimum requirement of credits in **maximum period of seven (07) years** as per M.Tech Ordinances and Regulations in order to qualify for award of Degree. The PG Degree is awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS). Starting from the academic session 2024-25, degrees are proposed to be awarded by NIELIT Deemed to be University, pending approval. Students will have the option to select the awarding university (i.e., BAMU or NIELIT Deemed to be University).



7.0 Master of Science in Computer Science with specialization in Data Science M. Sc. (CS with DS)

1. M. Sc. (CS with DS) is an integrated course comprising two parts spread over two years. Each part will consist of two semesters. The course of study of M. Sc. (CS with DS) shall be divided in four semesters and university examination will be held at the end of every semester in the months of November/December (for semester I & III) and May/June (for semester II & IV) or as fixed by the Academic Council.
2. A candidate must complete and pass the whole course of two years within a maximum of three years from the date of admission in M. Sc. (CS with DS) first semester.
3. The outlines of tests and syllabi shall be such as prescribed by the Academic Council from time to time.
4. A candidate will be eligible to join 1st semester of M.Sc. (CS with DS) course, if he/she has passed graduation in Computer Science/Computer Application/Information Technology or graduation with mathematics or statistics thereto without reappear.
5. Semester examination will be open to regular candidates who have been on the rolls of a college affiliated to this University and meet the attendance and other requirements as prescribed in the Ordinances No.7
6. Subject to fulfilment of requirement of House examinations, the attendance requirements and these

ordinances there will be no condition of passing papers for promotion from odd semester to even semester in an Academic Session.

To qualify for admission to 2nd year of the Course, the candidate must have passed 50% of total papers of the two semesters of the 1st year.

A candidate placed under reappear in any paper, will be allowed two chances to clear the reappear, which should be availed within consecutive two years/chances i.e. to pass in a paper the candidate will have a total of three chances, one as regular student and two as reappear candidate.

The examination of reappear papers of odd semester will be held with regular examination of the odd semester and reappear examination of the even semester will be held with regular examination of even semester. But if a candidate is placed under reappear in the last semester of the course, he will be provided chance to pass the reappear with the examination of the next semester, provided his reappear of lower semester does not go beyond next semester.

7.1 Attendance Requirements

Every candidate will be required to attend a minimum of 75% lectures delivered to that class in each paper as well as 75% of the laboratory work, seminars etc. separately. Provided that a deficiency in attendances may be condoned for special reasons, as per the relevant ordinances on the subject.

- To be eligible to appear in the semester examination a candidate must have obtained in the house examination at least 25% marks in each paper; 33% marks in the aggregate of all subjects of the semester. The Principal at his discretion may allow a special test to a candidate who could not appear in the House examination owing to unavoidable reasons or fails to secure the minimum marks as prescribed above.
- Late College Students: A candidate who has completed the prescribed course of instructions for a semester but has not appeared in the examination or having appeared, has failed in the examination, may appear as a late college student within the prescribed period.
- Amount of examination fee to be paid by a candidate for each semester shall be as fixed by the University from time to time.
- Applications for admission to the examination shall be made on the prescribed form attested by the competent authority as per University rules. The last date by which admission forms and fees must reach the Registrar shall be as follows:
- University medal will be awarded to a candidate who secured first position in the University on the basis of the marks of all the six semesters taken together. The general rules and conditions of the University for the award of medal/prizes etc. will be applicable in the award of University medal to the topper of this examination.
- The medium of instructions and examination will be English.
- In each Paper 25% of the total marks are assigned to the internal assessment and 75% marks to the University examination.
- The minimum number of marks required to pass the examination in each Part shall be 35% in each subject, provided that in subject with practical the percentage shall be required separately in written and practical/lab work. The candidate shall also be entitled to grace marks as admissible under the ordinances relating to the `GENERAL GRACE MARKS`.
- The successful candidates shall be classified on the basis of aggregate marks secured in all the six semesters of B. Sc. taken together as under:
 - a) 75% or more with Distinction.
 - b) 60% or more in the First division.
 - c) 50% or more but less than 60% in the Second division.
 - d) below 50% in the Third division.

Semester Examination	Without late fee	With Rs. 800/- Late fee	With Rs. 1200/- Late fee	With Rs. 5000/- Late fee	With Rs. 10,000/- Late fee
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December/January	30 th September	15 th October	21 st October	31 st October	10 th November
April/May	28 th February	15 th March	21 st March	31 st March	15 th April

8.0 Certified Programmes

8.1 Internship

NIELIT Aurangabad invites applications towards the NIELIT Internship-2024(Hybrid Mode) under Chip to startup program(C2S) of Ministry of Electronics & Information Technology(MeitY), Government of India from passionate and dynamic students from various institutes across India. This programme is created in such a way that the student will gain substantial knowledge, in the opted field, within a specific duration. In the course of their internship at NIELIT Aurangabad, the intern has the chance to carry out novel research and contribute to development initiatives under the supervision of research-oriented mentors, in order to enhance their research experience and exposure to the research and innovative world. The NIELIT Internship Program offers a transformative experience for students, empowering them with the skills, knowledge, and confidence to thrive in today's technology-driven world. By bridging the gap between academia and industry, this program prepares interns for successful careers and contributes to the growth and innovation of the technology sector.

Key Areas of Internship:-

- ☐ VLSI Design.
- ☐ Electronic Product Design.
- ☐ SoC(System on Chip) Design.
- ☐ FPGA Architecture & Programming.
- ☐ Embedded Systems & IoT.
- ☐ Drone UAS Technology.
- ☐ AI & Data Science.

Eligibility:-

Undergraduate – B.Tech./B.E./B.Sc./BCA etc. undergoing/Completed.

or

Postgraduate – M.Tech./M.E./M.Sc./MCA etc. undergoing/Completed.

Internship Registration Fees:-

Registration Fees for NIELIT Internship-Rs.5000/-.Fees once paid will not be refunded.

How to apply: -Students can fill the application form as given below:

a) Online application form can be filled from via Google form Link given below:

https://docs.google.com/forms/d/1ZDis-RUQtwnoJJTqHpDmese4z1TrleH-9e0kJZnfl8/viewform?edit_requested=true

b) Self-attested Scanned copy of updated CV/Resume.

c) Self-attested Scanned copy of Bonafide Certificate and NoC from the Institute where the student has undergone/is undergoing the current course, in the prescribed format.

d) Self-attested Scanned copy of Valid Government issued ID proof.

Duration: -

Duration of Internship: 4 – 8 weeks

Offline Internship is mandatory for every student at the NIELIT Aurangabad campus for at least 2weeks.

Certificates: -

E-certificate is issued only to the Interns on successful completion of the Internship and upon submission of the Internship Report to their Mentors.

Contact: -

Phone: 0240-2982021 Extn-241/126

E-mail: nielitinternshipC2S@gmail.com

8.2 Certification courses

NIELIT Aurangabad is also offering following certification courses:

- a. PG Diploma Courses in VLSI and Embedded Systems
- b. PG Diploma Courses in SoC Design and Verification
- c. Certified VLSI Design Engineer
- d. Internet of Things (IoT) Associate

For more information please visit <https://nielit.gov.in/aurangabad/index.php>

9.0 Partial List of Teaching Staff

Table no. 07

Sr No	Name of Faculty	Designation	Educational Qualification	Brief Profile
1.	Dr. Jayaraj Kidav	Scientist/Engineer 'E'	Ph.D.(Electronics & comm. Engg-VLSI Signal Processing)	Overall 18 years of experience in research and teaching. He is a principal investigator in Ultrasound System Development Project, SMDP-C2SD Project and CARS/NRB Project
2.	Sh. Sasi Kumar Gera	Dean (Academics)& Scientist F	M.Tech (Manuf. Engg) , B.Tech(Mech. Engg)	Alumnus of IIT Madras, in his professional career spanned over 24 years he is instrumental in initiating many out-of-box research works in areas of CAD/CAM, CNC Machines, Industrial Robots Machine vision, Industrial Design. He has implemented real-time robot path control by using image processing for seam-less welding applications at the University of Liverpool, England as research associate (UNDP/UNIDO fellowship). He has guided several projects at Masters level. His research interests are Industry 4.0, Autonomous Robots, CAD/CAM, Lean Manufacturing.
3.	Dr. Lakshman K	Dean (Skill Development), and Scientist D	PhD, M. Tech, B. Tech.(CSE)	In his professional career spanning over 12 years, he has executed many Government Projects of IT Mission (Kerala), Ministry of Social Justices, and ISEA project etc. His areas of areas of interest are Thermal Image Processing, Blockchain, Cyber Security, Mobile Application Development and Software Engineering. He is also working as a Placement Officer and Nodal Officer of Model Career Centre.
4.	Sh. Saurabh Kesari	Scientist C	B.Tech (Electronics and Tele-Communication Engineering)	More than 6 years of industrial experience in Embedded and IOT Hardware design and development
5.	Chaitanya Narayan Kadadas	Scientist C	B.E.(Electronics and Tele-Communication Engineering)	Having 7 Years' experience in fiber optics communication and networking
6.	Sh. Saurabh Bansod	Scientist C	M.Tech (Electronics & Instrumentation) B.E.(Electronics)	Alumnus of N.I.T. Rourkela, In the span of 6 years he has done tremendous work in the area of Industrial Automation. His area of interest includes data acquisition using NI DAQ cards, Machine Learning on Embedded Systems.

7.	Dr. Anirban Jyoti Hati	Scientist C	PhD, M.Tech, B.Tech	With expertise in Electronics System design, he has experience of teaching advanced microcontroller and microprocessor. He has sound knowledge of Deep learning and Machine learning. He is also doing projects on Artificial Intelligence, Computer Vision and Embedded Systems.
8.	Sh. Yogesh Kumar	Scientist 'B'	B.Tech (CSE)	A young scientist who possesses expertise in the area of Internet of Things, Scripting languages like Python. His other areas of interest are Cyber Security, Web Application Development.
9.	Sh. Shashank Kumar Singh	Scientist 'B'	B.Tech. (Electronics and Communication Engineering)	A young scientist who possesses expertise in the area of VLSI. More than 1 years of teaching experience.
10.	Sh. Balasaheb. B. Sorte	Sr. Tech. Officer	DME, (DCS&M)	More than 20 Years' experience as Trainer / faculty of CAD/CAM/CAE including 08 Years of experience as Faculty for Mechanical Design and Developments and Workshop Technology. He is looking after Design & Development of Electro-Mechanical products under Academic activities of DEPM & B.Tech & M.Tech courses
11.	Sh. Milind S Kshirsagar	Sr. Tech. Officer	Diploma (Ind. Electronics)	He is highly motivated and experienced faculty who specializes in Electronics.
13.	Sh. Ritesh Prasad	Senior Technical Assistant	M.Tech (Electronics and communication Engg.)	With expertise in Electronics System design, he has experience of teaching various subjects of electronics. His area of interest includes Microelectronics and Embedded Systems.
14	Sh. Ravi Ranjan Singh	Senior Technical Assistant	M.Tech (Electronics and communication Engg.)	With expertise in Electronics System design, he has experience of teaching various subjects of electronics. His area of interest includes VLSI Design and Embedded Systems.

10.0 Placement Assistance and Support:

Students of the Centre are trained to become R&D engineers. In Course curriculum there is emphasize on Innovation, Design and Development of Electronic Product. The Centre has also signed MoU with Chamber of Marathwada Industries and Agriculture (CMIA) to platform to Startup Aspirants students. In association with Directorate of Employment, Ministry of Labour & Employment (MoLE), a Model Career Centre is also functioning to provide a variety of employment related services. Apart from this an independent Placement Cell is providing Placement support and assistance to all the students. Almost all the students of the Institute gets career opportunities of their choice.

1. MoU with CMIA



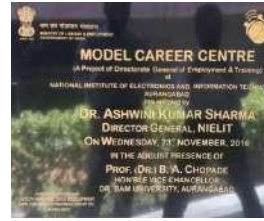
CMIA is a group organization representing around 650 small scale / medium scale / large scale industries including the Multinational Companies (MNC's) of the Maharashtra. An MoU was signed with CMIA in November, 2017 for referral of Students Projects which have potential of developing into scalable business models and also adoption of their business startup ideas. The support for internship and employment to our students in member companies of CMIA was another goal of the MoU.

2. NIELIT and CMET signed MoU



NIELIT and C-MET signed an MoU for technical collaboration and exchange of ideas for R&D and skilling in the emerging areas of IECT.

2. Model Career Centre



In association with Directorate of Employment, Ministry of Labour and Employment (MoLE), NIELIT is providing a variety of employment

related services to students of the region. The students of the institute are by default members of National Career Services (NCS) of Government of India. The Model career Centre apart from organizing multiple Job Fairs every year is also conducting counseling sessions to improve Soft Skills and presentation skills of the students. Leading Experts and Industrialists are invited for the counseling sessions to share their views.

3. Industrial Tie-up

Multiple visits of the students are arranged in leading Industries of the region so that they can get well versed with current Industrial trends. The students also get a chance to take up real Industrial issues as their project work. The bright students are also provided with mentoring support for establishment of their own start-up by Industrialists.

4. Some of Companies who have come from Campus Placement



11.0 Some of the Alumni of the Centre

NIELIT Aurangabad believes in developing and maintaining a strong alumni association for its growth and progress

Table no. 08

1. Dr. Suresh D. Shirbahadurkar  Professor Zeal College of Engineering, Narhe, Pune	<i>“Progress Seminars Conducted by NIELIT Aurangabad research centre and attended by fellow researches, M.Tech Students & eminent guides provided me a forum to present and discuss my research. All the IEEE publications were available for reference. The state of art lab facilities were available for simulation & experimentation.”</i>
2. Dr. Radhakrishna Naik  Vice Principal G S Mandals Maharashtra Institute of Technology Aurangabad	<i>“The industrial Design & Product Design subjects helped me to compliment my class room learning with in-depth project work. Guest lectures arranged at NIELIT exposed us to latest trends in industry & real life problems. Industrial visits helped us to understand & relate our subject to industrial environment”</i>
3. Dr Varsha Ratnaparkhe  Assistant Professor & Dean (Quality Assurance), Department of Electronics & Telecommunication Engineering, Government College of Engineering, Aurangabad	<i>“Skills that I acquired and honed while in NIELIT Aurangabad, are benefiting me continuously in my professional career. NIELIT has helped me shape my character and strengthened attitude required to deliver strong results in academia.”</i>
4. Mr. Sumit Wankhad  VLSI Engineer, Cerium Systems, Bangalore	<i>“I found, NIELIT Aurangabad atmosphere conducive for learning. NIELIT Aurangabad helped in building strong fundamentals with deeper understanding in Electronics Product Design. A number of facilities including labs were accessible to students. Faculty were readily available to solve any study related difficulties and staffs were very kind in resolving any related issues.”</i>

5. Dr. Alka Mahajan  Director Nirma University, Indore	<i>“NIELIT Aurangabad taught me to think critically and confidently in experimental and theoretical situations. I developed professionally and made a wealth of friends and resources”.</i>
6. Mr. Jaykumar H Prabhakar  Vice President (Global lead incident management) at Accenture, Thane. Also a Member of ISKCON working with NGO for Swachh Bharat	<i>“What makes this course unique are the subjects in product design and PCB design which helped me understand the whole process of product development Making of the product was a very creative experience with starting from design, to manufacturing of PCB in the PCB Lab & then making the enclosure in the workshop, not to forget the innumerable sketches we made of the various versions & forms of the product.”</i>
7. Arvind B Nyayadhish  Director Enman Automation Pvt.Ltd, Aurangabad	<i>“To shine into day's competitive world it is very essential to have the nurturing that helps you go the extra mile. NIELIT Aurangabad equipped me for the world outside with the best skill set. Those amazing years gave me much more than book is h knowledge; Imetprobably the best people in my life and some inspiring personalities Proud to be an alumnus of NIELIT Aurangabad.”</i>
8. Mr. MahendraPadalkar  Principal Technical Architect (Cloud)at Tech Mahindra Ltd, Pune	<i>“In NIELIT Aurangabad, I acquired and honed not only technical skills but also management and people skills that are assisting me immensely in my career. I’m thankful to NIELIT Aurangabad for providing such a strong foundation towards my career.”</i>
9. Mr. Pradeep Kizhiseeri  Senior Consultant Presently into Hatstand, Singapore	<i>“NIELIT Aurangabad is where the students are molded to Perfect Industry Professionals & Entrepreneurs. The reputation and brand equity associated with the institute makes one feel proud. Thanks to college management and faculty for engineering my career in right direction.”</i>

10. Mr. Sandeep K Patni  Co-Founder and VP of Systems and Engineering at CumuLogicInc, NewJersey,USA	<i>“NIELIT Aurangabad taught me that education can be the most challenging, extremely rewarding, exciting, and fun. I learned that passion for learning really is the driver of finding new knowledge, and that passion is honestly contagious.”</i>
11. Mr. Rupesh Kollale  President & Director Endress+Hauser InforServe (India) Pvt. Ltd. Aurangabad	<i>“NIELIT (CEDTI), provides the perfect platform for students to excel ‘Beyond the theory’ to work and experiment with latest technological things, this is a world class environment right here in Aurangabad where there is freedom to hone your practical skills which are very important along with the theory. We had very supportive and passionate teachers who made us what we are today. I am immensely thankful to NIELIT for shaping my life.”</i>
13. Mr. Hrushikesh Gangur  Sr. Solutions Architect, Amazon Web Services, San Francisco, California	<i>“It is close to 25 years, and I still have memories of those golden days that made me a complete man. The education, labs and end-to-end understanding of product lifecycle learnt for CEDTI(now known as NIELIT) still help me in day-to-day work and life. I am blessed that I had my education done from this institute. I wish other engineering colleges have a same bar raising faculties, facilities, and methodology as NIELIT has to build quality engineers for India.”</i>

12.0 Fee-Structure for Academic year (2024-25)

Table no. 9

Sr. No.	Course Name/ Hostel	Particulars	One-time fee (To be paid during admission along with Odd Sem fees)	Odd Sem Fees (Rs.)	Even Sem Fees (Rs.)
1	DEPM 1 st year	Tuition Fee	--	26730/-	26730/-
		Student Development Fees	--	1800/-	-----
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	--	
		Caution Money *	1250/-	--	--
		Sub Total	1450/-	28,830/-	27030/-
2	DEPM Lateral Entry	Tuition Fee	--	26730/-	26730/-
		Student Development Fees	--	1800/-	-----
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	--	
		Caution Money *	1250/-	--	--
		Sub Total	1450/-	28,830/-	27030/-
3	B.Tech 1 st year	Tuition Fee	--	50000/-	50000/-
		Student Development Fees	--	4100/-	-
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	-	-
		Caution Money *	1000/-	--	-
		Sub Total	1200/-	54400/-	50300/-
4	B.Tech Lateral Entry	Tuition Fee	--	50000/-	50000/-
		Student Development Fees	--	4100/-	-
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	-	-
		Caution Money *	1000/-	--	-
		Sub Total	1200/-	54400/-	50300/-
5	M. Tech. Full Time 1 st year	Tuition Fee	--	60,000/-	60,000/-
		Student Development Fees	--	4100/-	--
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	--	--
		Caution Money *	1000/-	--	-
		Sub Total	1200/-	64400/-	60300/-
6	M. Tech Part Time 1 st Year	Tuition Fee	--	60,000/-	60,000/-
		Student Development Fees	--	4100/-	--
		Mark sheet Printing charges	--	300/-	300/-
		ID card and Library Card	200/-	--	--
		Caution Money *	1000/-	--	-
		Sub Total	1200/-	64400/-	60300/-
7	Boy's Hostel Accommodation	Hostel Fee per Semester (5 months)	--	11050/-	11050/-
		Hostel Deposit*	1000/-	--	--
		Sub total	1000/-	11050/-	11050/-

***Caution Money is onetime non-refundable payment and transferred to Student Alumni Fund on completion of the course.**

Note: Mess Service are compulsory for Hostel Students. Outside food within the premises is not permissible. Mess charges are to be paid directly to mess manager on monthly basis.

Important:

- i. Presently, SC/ST students – only Tuition Fee is met from SCSP/TSP Scheme of MeitY (subject to the terms and conditions and availability of this scheme). Hence tuition fees for SC/ST student is waived.
- ii. There shall be an increase in tuition fee and hostel fee for every academic year.
- iii. The student shall have to complete the registration formalities including full payment of hostel fees and tuition fees within one week of the commencement of registration process before every semester starts. Last date of registration will be notified in academic calendar with approval of executive director.

10.1 Refund of fees in the event of cancellation of admission:

1. Fees (Tuition fees, Hostel fee, caution money, Student Development Fees, Marksheet, ID card and Library card charges) once paid will not be refunded under any circumstances.
2. In case of any discrepancy, the decision of Executive Director, NIELIT Aurangabad in respect of refund of fees in the event of cancellation of admission will be final.

ANNEXURE I

**Tentative Academic Calendar for M. Tech (EDT), B.Tech (EE) and DEPM Programs
Academic Year 2024-2025 (Semester I)**

Sr. No.	Semester-I	
1	Start of Online application	15 th May 2024
2	Last date of Online application	11 th July, 2024
3	Classes Begin	1 st August 2024
4	Class Test-1	15 th Sep 2024*
5	Class Test-2	14 th Oct 2024*
6	Classes End	31 st Oct 2024*
7	Semester (Practical) Examination	1 st Nov. 2024*
8	Semester (Theory) Examination	10 th Nov. 2024*
9	Project Assessment	9 th Dec 2024*
	Project Feasibility Seminar	
10	Semester Break (DEPM and B.Tech)	19 th Dec 2024 to 5 th Jan 2025*
11	Declaration of Results	5 th Jan 2025*

* Tentative Dates, can change time to time as per approval of the authority.

ANNEXURE – II
Assessment Computation of SGPA & CGPA

1. **Semester Grade Point Average (SGPA)** is the weighted average of Grade Points obtained by a student in a semester and is computed as follows:

$$SGPA = \frac{U_1 \times M_1 + U_2 \times M_2 + \dots + U_n \times M_n}{U_1 + U_2 + \dots + U_n}$$

Where U₁, U₂, are subject credit of respective course and M₁, M₂, are the marks Obtained in the respective subject out of 10 (grade point)

Note: The semester grade point average (SGPA) for all the six semester is also mentioned at the end of every semester.

2. **The Cumulative Grade Point Average (CGPA)** is used to describe the overall performance of a student in the Diploma/Degree course and is computed as under:

$$CGPA = \frac{\sum_{n=1}^k (SGPA_n \times C_n)}{\sum_{n=1}^k (C_n)}$$

Where SGPA (n) is the nth Semester SGPA of the student and C_n is the nth Semester total credit. The value of k is as under

- a. Six (6) in case of DEPM Program
- b. Eight (8) in case of B.Tech Program
- c. Four (4) in case of Full-time M. Tech Program and M.Sc program.
- d. Six (6) in case of Part-time M. Tech Program.

Note: The Semester and Cumulative GPA are rounded off to the second place of decimal

ANNEXURE III

NIELIT Aurangabad, Maharashtra (India)			
Application form			
DEPM (Diploma in Electronics Production and Maintenance)			
First Year Admission[] /Lateral Entry Admission []			
To, The Dean Academics, NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004 (MS)		Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm)	
Sir/Madam, I have passed SSC X th /XII th standard/ Higher Secondary Certificate (HSC) Examination passed from a recognized Board with Physics, Chemistry, Mathematics OR ITI (Electrical / Electronics) from recognized Institute. I am hereby applying for First Year/ Direct admission to 3 rd Semester Diploma in Electronics Production and Maintenance (DEPM) Course during the Academic year 2024-2025 and request you to kindly allow me to appear in the selection test for the said course at NIELIT Aurangabad. I submit my particulars as under:			
Name of Candidate:			
Mother's Name			
Father's Name:			
Date of Birth:			
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]			
Address for Correspondence:			
		Pin:	
E-mail ID:		Landline No.:	Mobile No.:
Total % marks in ITI or 12th standards subjects			
ITI Year	% Marks	12th standards subject	% Marks
1 st Year		Physics	
2 nd Year		Chemistry	
		Maths	
		Vocational Training	
Average of above two years		Average of any above three subjects	

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to todepm-abad@nielit.gov.in
4. Please attach the scan copy of fee deposit counter along with application form

Website: www.nielit.gov.in/aurangabad

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. No refund of Application form Fees.
4. Mail scanned copy of filled form & fee receipt to respective E-mail: depmandabad@nielit.gov.in, btech-abad@nielit.gov.in, mtech-abad@nielit.gov.in

I hereby apply for grant of eligibility and admission as an International Student to Diploma in Electronics Production and Maintenance (DEPM) /B.Tech (EDT) / M.Tech course during the Academic year and request you to kindly grant me a certificate of eligibility and admission to the said course in NIELIT Centre, Aurangabad, Maharashtra (India). I submit my particular asunder:

Course Applied:

Passport Size Recent

Last Name: First Name : Middle Name : Mother Name : Date of Birth : DD/MM/ YYYY Nationality:

Name: First Name / Middle Name / Surname (Date of Birth: DD/MM/YY) (Nationality):																			
ADDRESS FOR CORRESPONDENCE																			
												Pin:							
E-mailID:Tel No. (withISD/STD)																			
ACADEMIC QUALIFICATIONS (in ascending order)																			
Sr. No.	Examination Passed	Name of School / College	Name of Examining body (Board / University)	Year of Passing	% Marks obtained	Class / Division													

ANNEXURE-IV (B)

(To be typed on Rs.100/- Stamp Paper) Declaration and Undertaking

1. I hereby declare that I have carefully read the information brochure for the academic year 2024-2025.
2. I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby.
3. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the NIELIT Centre, Aurangabad and I agree to abide by them.
4. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible.
5. I undertake to furnish the necessary certificate(s)/ document(s)/ paper(s) in original along with a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the NIELIT Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Foreign National)

Date:

Place:

ANNEXURE-IV (C)

(To be typed on Rs.100/- Stamp Paper) Declaration and Undertaking

1. I hereby declare that I have carefully read the information brochure for the academic year 2024-2025.
2. I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby given in information brochure.
3. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the NIELIT Centre, Aurangabad and I agree to abide by them.
4. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible.
5. I undertake to furnish the necessary certificate(s) / document(s) / paper(s) in original along with a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the. NIELIT Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Candidate)

(Name & Signature of Guardian/Parent)

Date:

Place:

ANNEXURE V

NIELIT Aurangabad, Maharashtra (India)				
Application form				
Lateral Entry Admission (Direct 2nd Year) of 4 Year's B.TECH (Electronic Engineering)				
To, The Dean Academics, NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004 (MS)		Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm)		
Sir/Madam, I have passed 3 year diploma course in Electronics & allied streams with minimum 45% marks (40 percent for SC/ST candidates) approved by AICTE. I hereby register for admission to 3rd Semester B. Tech (Electronic Engineering) Course through Lateral Entry during the Academic year 2024-25 and request you to kindly grant me admission to the said course at NIELIT Aurangabad. I submit my particular as under:				
Name of Candidate:				
Mother's Name				
Father's Name:				
Date of Birth:				
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]				
Name of the Institute/Beneficiary		National Institute of Electronics and Information Technology(NIELIT)		
Name of the Bank		State Bank of India		
Branch		Samarth Nagar Aurangabad Maharashtra		
Saving Bank Account Number		32078399585		
IFSC/RTGS NO		SBIN 0007919		
Application Fee		The application fee is Rs.1000/-. However, the candidates belonging to SC/ST/PWD are exempted from application fees.		
Mode of Electronic Transfer		NEFT, SBICollect, Website: www.onlinesbi.com		
Address for Correspondence:				
		Pin:		
E-mail ID:		Landline No.:	Mobile No.:	
Total % marks in Diploma Course in Engineering and Technology				
Branch/Discipline	1st Year %	2nd Year %	3rd Year %	Avg. %

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to [**btech-abad@nielit.gov.in**](mailto:btech-abad@nielit.gov.in)
4. Please attach the scan copy of fee deposit counter foil along with application form.

ANNEXURE VI

NIELIT Aurangabad, Maharashtra (India)		
Application form		
2 year/ 3 Year's M. Tech (Electronics Design Technology) Full time/ Part Time Course Admission		
To, The Dean (Academics), NIELIT Aurangabad, Dr. BAM University Campus, Aurangabad, 4310004(MS)	Passport Size Recent Photograph Attested (Size: 4.5x5.5 cm)	
Sir/Madam, I have passed B.E./ B .Tech Degree or equivalent in Electronics/Electrical/Telecommunication / Instrumentation engineering etc. from a recognized University with a minimum of 55 percent marks (50 percent for SC/ST candidates). I am hereby applying for the admission to M. Tech (Electronics Design Technology) Full Time/ Part time course during the Academic year 2024-2025. I submit my particular as under:		
Name of Candidate:		
Mother's Name		
Father's Name:		
Date of Birth:		
Category [General/SC/ST/OBC /PWD(General)/PWD(OBC)]		
Sponsorship by Academic Institute/Industry		
Working Experience		
Approximate Distance of Working place from the Institute(Kms)		
Name of the Institute/Beneficiary	National Institute of Electronics and Information Technology(NIELIT)	
Name of the Bank	State Bank of India	
Branch	Samarth Nagar Aurangabad Maharashtra	
Saving Bank Account Number	32078399585	
IFSC/RTGS NO	SBIN 0007919	
Mode of Electronic Transfer	NEFT, SBI Collect, Website: www.onlinesbi.com	
Application Fee	The application fee is Rs.500/-. However, the candidates belonging to SC/ST/PWD are exempted from application fees.	
Address for Correspondence:		
	Pin:	
E-mail ID:	Landline No.:	Mobile No.:

Total % marks in B. Tech/B.E Degree in Electronics Engineering or allied streams					
Stream/Discipline	1st Year %	2nd Year %	3rd Year %	4th Year %	Avg. %

Tick Appropriate cell based on category and hostel accommodation		
Amount to be paid on counseling for admission(1 st Semester M.Tech Fee & Deposits)		
Category	Institute Fee	Institute Hostel fee (in case hostel required)
All other than SC/ST	Rs: 49,250/-	Rs: 12000/-
SC/ST	Rs: 1250/-	Rs:12000/-

Following documents with one self-attested copy of each document to be handed over to NIELIT Aurangabad academic section at the time of admission depending upon the category to which the candidate belongs.

Documents (Original with Self attested copy)	Certificate No. & Date of Issue	Yes/No	Checked (By Office)
Two Passport Size Photograph (attested)			
Date of Birth proof(X Mark-sheet / Certificate)			
All Mark Lists of the Qualifying Examination			
Degree Certificate of the Qualifying Examination.			
Conduct Certificate from the College, where the student has last studied			
Transfer Certificate from the college last studied.			
Migration Certificate (In case students are from other university)			
Physical Fitness Certificate (as per given format)			
Non creamy layer OBC certificate valid up to 31st March 2025, as per given format (OBC candidate only)			
SC/ST Certificate as per given format (SC/ST candidate only)			
Physical With Disability Certificate as per given format (PWD candidate only)			
Sponsorship Certificate and Experience Certificate (Sponsored Candidates Only)			

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information. I am ready to remit Rs./- to bank as per the details given above today itself to secure the admission.

Candidate Signature with date

Important Instructions

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. Mail scanned copy of filled form & fee receipt to mtech-abad@nielit.gov.in
4. Please attach the scan copy of fee deposit counter foil along with application form

ANNEXURE VII
Prescribed Performa for SC/ST Certificate (for B.Tech/M.Tech)

This is to certify that Shri/ Shrimati/ Kumari* _____ son/daughter of _____
of _____ village/town/* in _____ District/Division* of the _____ State/Union

**Territory* belongs to the Caste/Tribe* which is recognized as a Scheduled Castes [SC]*
/ Scheduled Tribes [ST]* under:**

The Constitution (Scheduled Castes) Order, 1950

The Constitution (Scheduled Tribes) Order, 1950

The Constitution (Scheduled Castes) Union Territories Order, 1951

The Constitution (Scheduled Tribes) Union Territories Order, 1951

As amended by the Scheduled Castes and Scheduled Tribes Lists (Modification) Order, 1956, the Bombay Reorganization Act, 1960 & the Punjab Reorganization Act, 1966, the State of Himachal Pradesh Act 1970, the North-Eastern Area (Reorganization) Act, 1971 and the Scheduled Castes and Scheduled Tribes Order (Amendment) Act, 1976. [%]

The Constitution (Jammu & Kashmir) Scheduled Castes Order, 1956. The Constitution (Andaman and Nicobar Islands) Scheduled Tribes Order, 1959 as amended by the Scheduled Castes and Scheduled Tribes Order (Amendment Act), 1976. The Constitution (Dadra and Nagar Haveli) Scheduled Castes Order, 1962. The Constitution (Dadra and Nagar Haveli) Scheduled Tribes Order 1962**. The Constitution (Pondicherry) Scheduled Castes Order, 1964**. The Constitution (Scheduled Tribes) (Uttar Pradesh) Order, 1967**. The Constitution (Goa, Daman & Diu) Scheduled Castes Order, 1968**. The Constitution (Goa, Daman & Diu) Scheduled Tribes Order, 1968**. The Constitution (Nagaland) Scheduled Tribes Order, 1970**. The Constitution (Sikkim) Scheduled Castes Order, 1978**. [%]

The Constitution (Sikkim) Scheduled Tribes Order, 1978**. The Constitution (Jammu & Kashmir) Scheduled Tribes Order 1989**. The Constitution (SC) Orders (Amendment) Act, 1990**. The Constitution (ST) Orders (Amendment) Ordinance, 1991**. The Constitution (ST) Orders (Second Amendment) Act, 1991**. The Constitution (ST) orders (Amendment) Ordinance, 1996. The Scheduled Caste and Scheduled Tribe Orders (Amendment) Act. 2002. The Constitution (Scheduled Caste) Orders (Amendment) Act, 2002. The Constitution (Scheduled Caste and Scheduled Tribe) Orders (Amendment) Act, 2002. The Constitution (Scheduled Caste) Order (Amendment) Act, 2007. [%]

2. Applicable in the case of Scheduled Castes, Scheduled Tribes persons who have migrated from one State/Union Territory Administration.

This certificate is issued on the basis of the Scheduled Castes / Scheduled Tribes certificate issued to Shri/Shrimati _____, Father/Mother of Shri/Srimati/Kumari*

of village/town* in the District/Division* of the State/Union Territory* , who belong to the Caste/Tribe* which is recognized as a Scheduled Caste* / Scheduled Tribe* in the State/Union Territory* issued by the dated . %

3. Shri/Shrimati/Kumari* _____ and/or* his/her* family ordinarily reside(s) in the village/town* _____ of _____ District/Division* of the State/Union Territory of _____

Place

Date

(with seal of office)

*** Please delete the words which are not applicable**

**** Please quote specific presidential order**

Signature

Designation

% please delete the paragraph which is not applicable.

^ List of authorities empowered to issue Schedule Caste / Schedule Tribe Certificates:

- 1) District Magistrate /Additional District Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / 1st Class Stipendiary Magistrate / Sub-Divisional Magistrate / Extra-Assistant Commissioner / Taluka Magistrate / Executive Magistrate.
- 2) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.
- 3) Revenue Officers not below the rank of Tehsildar.
- 4) Sub-Divisional Officers of the area where the candidate and/or his family normally resides.
- 5) Administrator/Secretary to Administrator/Development Officer (Lakshadweep).

NOTES:

- 1) The term ordinarily reside(s) used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.

ANNEXURE VIII
OBC Caste Certificate Format (for B.Tech/M.Tech)

[This certificate MUST have been issued on or after 1st April 2024]

This is to certify that Shri/Smt./Kum. _____ Son/Daughter of Shri/Smt. _____
of Village/Town District/Division in the State/UT belongs to the Community which is recognized as a
backward class under:

- (i) Resolution No. 12011/68/93-BCC(C), dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186, dated 13/09/93.
- (ii) Resolution No. 12011/9/94-BCC, dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163, dated 20/10/94.
- (iii) Resolution No. 12011/7/95-BCC, dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88, dated 25/05/95.
- (iv) Resolution No. 12011/96/94-BCC, dated 9/03/96.
- (v) Resolution No. 12011/44/96-BCC, dated 6/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 11/12/96.
- (vi) Resolution No. 12011/13/97-BCC, dated 03/12/97.
- (vii) Resolution No. 12011/99/94-BCC, dated 11/12/97.
- (viii) Resolution No. 12011/68/98-BCC, dated 27/10/99.
- (ix) Resolution No. 12011/88/98-BCC, dated 6/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270, dated 06/12/99.
- (x) Resolution No. 12011/36/99-BCC, dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71, dated 04/04/2000.
- (xi) Resolution No. 12011/44/99-BCC, dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 21/09/2000.
- (xii) Resolution No. 12016/9/2000-BCC, dated 06/09/2001.
- (xiii) Resolution No. 12011/1/2001-BCC, dated 19/06/2003.
- (xiv) Resolution No. 12011/4/2002-BCC, dated 13/01/2004.
- (xv) Resolution No. 12011/9/2004-BCC, dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210, dated 16/01/2006.
- (xvi) Resolution No. 12011/14/2004-BCC dated 12th March, 2007 published in the Gazette of India Extraordinary, Part-I, Section-1, No. 67 dated 12th March, 2007.
- (xvii) Resolution No. 12015/2/2007-BCC, dated 18/08/2010.
- (xviii) Resolution No. 12015/2/2007-BCC, dated 11/10/2010.
- (xix) Resolution No. 12015/13/2010-BC-II, dated 08/12/2011.
- (xx) Resolution No. 12015/05/2011-BC-II, dated 17/02/2014.
- (xxi) Resolution No. 12011/6/2014-BC-II, dated 07/12/2016.
- (xxii) Resolution No. 12011/13/2016-BC-II, dated 22/12/2016
- (xxiii) Resolution No. 20012/1/2017-BC-II, dated 19/01/2017
- (xxiv) Resolution No. 12011/7/2017-BC-II, dated 31/07/2017

Shri/Smt./Kum. _____ and/or his family ordinarily reside(s) in the _____
District/Division of _____ State/UT. This is also to certify that he/she **does not belong to the persons/sections (Creamy Layer)** mentioned in Column 3 of the Schedule to the Government of India, Department of Personnel & Training O.M. No. 36 012/22/93-Estt.(SCT), dated 08/09/93 which is modified vide OM No. 36033/3/2004 Estt.(Res.), dated 09/03/2004, further modified vide OM No. 36033/3/2004-Estt. (Res) dated 14/10/2008, again further modified vide OM No. 36036/2/2013-Estt (Res) dated 30/05/2014.

Place

Date

(with seal of office)

NOTE:

(a) The term 'Ordinarily' used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.

Signature

Designation

(b) ^The authorities competent to issue Caste Certificates are indicated below:

(i) District Magistrate / Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / First Class Stipendiary Magistrate/Sub-Divisional magistrate / Taluka Magistrate / Executive Magistrate / Extra Assistant Commissioner (not below the rank of 1st Class Stipendiary Magistrate).

(ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.

(iii) Revenue Officer not below the rank of Tehsildar.

(iv) Sub-Divisional Officer of the area where the candidate and / or his family resides

ANNEXURE IX
Physical Disability Certificate (format)

Certificate No.

This is to certify that I have examined
Mr./MsSon/Daughter/Wife of
Mr.....Age.....
Gender.....on.....

Photograph Of the Candidate Showing the Physical
--

- i. He/ She is suffering from.....which comes under the sub category Blindness /Low vision/Speech & Hearing impairment/Orthopedic disorder/Learning disabilities. Dyslexia, Dyscalcula, Dysgraphica, Spastic.
- ii. The percentage of disability is. %.
- iii. The disability is permanent innature.
- iv. This condition is progressive/non-progressive/likely to improve/not likely to improve. Reassessmentofthiscaseisnotrecommended/isrecommendedafteraperiodofyears..... months.
- v. The candidate is capable of carrying out all activities related to theory and practical work as applicable to DEPM/ B.Tech/ M.Tech course of NIELIT Aurangabad without any special concession and exemptions.
- vi. This certificate is issued as per the provisions given in the Persons with Disability Act, 1995 and its amendment.
- vii. This Certificate is issued for the purpose of his/her admission to of DEPM/ B.Tech (ESE)/ M.Tech (EDT) course of NIELIT Aurangabad in the Academic Year 2024-25 at NIELIT Centre, Aurangabad (MS).

Date:

Place:

Director OR Dean
/Civil Surgeon

Seal of Institution/Hospital

ANNEXURE X

Physical Fitness Certificate (format)

(To be issued by a Registered Medical Practitioner)

GENERAL EXPECTATIONS				
Candidates should have good general physique. In particular,				
a Chest measurements should not be less than 70cm, with satisfactory limits of expansion and contraction.				
b Vision should be normal. In case of defective vision, it should be corrected to 6/9 in both eyes or 6/6 in the better eye. Colour blind and uniocular persons are restricted from admission.				
c Hearing should be normal. Defective hearing should be corrected.				
d Heart and lungs should not have any abnormality and there should be no history of mental illness and epileptic fits.				
Name of the candidate:				
Identification Mark (a mole, scar or birthmark), if any				
Major illness/operation, if any (specify nature of illness/operation)				
Height in cm:		Weight in kg:		Blood Group:
Past History	(a) Mental illness (b) Epileptic Fit			
Chest		(a) Inspiration in cm		(b) Expiration in cm
Hearing				
Vision with or without glasses:	Right Eye	Left Eye	Colour Blindness	Uniocular vision
Respiratory System				
Nervous System				
Heart		a) Sounds	b) Murmur	
Abdomen Liver Spleen		Hernia	Hydrocele	
Any other defects:				
Certificate of Medical Fitness (tick appropriate box below)				
☐	The candidate fulfils the prescribed standard physical fitness, medical fitness and is fit for admission to DEPM/B.Tech/M.Tech course of NIELIT Aurangabad.			
☐	The candidate does not fulfill the prescribed standard of physical fitness/medical fitness and is unfit/temporarily unfit for admission due to following defects:			
☐				
Name of the Doctor		Signature with date	Registration number	Seal

ANNEXURE XI

Sponsorship Certificate

(On letterhead of the Institute /Organization)

Outward No.:

Date: _

To,

The Dean Academics,
NIELIT Aurangabad
Dr. B A M University Campus,
Aurangabad 431 004

This is to certify that Mr. / Ms is serving in
our Organization / Institution as.....since.....

The Organization / Institution has no objection for the mentioned candidate to join the M.Tech (EDT)
at NIELIT, Aurangabad and will permit the candidate to attend lectures as per the institute timetable.

The Organization / Institution will render all possible help to him / her in persuasion of studies.
He / She will be relieved for a requisite period, if selected for the course.

Signature of Competent Authority

Name: Designation:

Seal of Sponsoring Organization

ANNEXURE XII

No Objection Certificate For M.Tech (EDT) Part Time Candidate (On letterhead of the Institute /Organization)

Outward No.:

Date: _

To,

The Dean Academics,
NIELIT Aurangabad
Dr. B A M University Campus,
Aurangabad 431 004

This is to certify that Mr. / Msis serving in our
Organization / Institution as.....since.....

The Organization / Institution has no objection for the mentioned candidate to join the M.Tech (EDT)
Part time Course at NIELIT, Aurangabad.

The Organization / Institution will render all possible help to him / her in persuasion of studies.He/ She
will be relieved for a requisite period, if selected for the course.

His/ Her working place is within 60 km distance from the institute. If candidate resigns or transferred
beyond 60 km distance, the intimation in this regard will be given to your office in writing within a week.

Signature of Competent Authority

Name:

Designation:

Seal

ANNEXURE-XIII CAD/CAM LAB

3. Colour Plotter

Objectives

The lab is aimed at giving exposure to and enhancing the knowledge and skills of engineers involved in the operation use of CNC machines, CAD/CAM packages and for those who want to provide training to others in this area. It gives exposure and on hand experience in the field of CAD/CAM and CNC machines, Computer Integrated Manufacturing and Industrial Robots.

CAD/CAM Lab is equipped with the latest Machines of for CNC machining, also has flagship CAD/CAM software packages and high end CAD/CAM workstations to meet the present industrial requirements. This Lab equipped with latest professional A0 width colour Plotter with paper roll feeder that makes realistic printout of colour CAD models and drawings. Also equipped with an 85 inch smart interactive digital display with various connectivity for Lab cum class room delivery.

Some of the facilities available as follows:

Main Equipment's Available

CNC Lathe Machine



Lathes are machines that cut work pieces while they are rotated. CNC lathes are able to make fast, precision cuts, generally using indexable tools and drills with Automatic Tool Changer. It has Graphic simulation for product proving.

CNC Milling Centre



CNC mills use computer controls that are able to translate programs consisting of alpha-numeric codes to move the spindle (or workpiece) to various locations and depths to cut materials. It has Automatics tool turret with 6 tools and Graphic simulation for product



It prints professional-quality full colour real scaled drawings, models as per CAD at your desk. This plotter is equipped with roll feeder to allow print larger lengths drawings easily with the width A0 size.

4. Vacuum Formation



Vacuum forming is a simplified version of thermoforming, where a sheet of plastic is heated to a forming temperature, stretched onto a single-surface mold, and forced against the mold by a vacuum. Vacuum-formed components can be used in place of complex fabricated sheet metal, fiberglass, or plastic injection molding.

5. CAD/CAM Software



Major CAD/CAM Software available for this Lab

- Catia V5
- Creo (Pro/Engineer)
- MasterCAM
- AutoCAD

6. Some of the Practical Project Experiments

1. Creating 2D and 3D Models using CAD
 2. CNC Part programming through CAD/CAM
 3. Machining of complex parts using CNC Machines
 4. 3D printing the products of created models
 5. Scanning the parts to obtain 3DCAD models
- Surface Machining and Product proving.

ANNEXURE-XIV CONSUMER ELECTRONICS LAB

Objective:

Consumer Electronics Lab has been established to provide hands on skills to the students that employers are seeking in Electronics Hardware & Productions as per Industrial requirements and standards. In various verticals of Consumer Electronics aspiring Maintenance Technician, Supervisor & Design Assistants are being skilled.

Main Equipment's available:

1. Rigol DS1104Z-S 100 MHz Digital Oscilloscope:



The DS1000Z 4 channel oscilloscopes come in 70 or 100 MHz versions with a 7 inch display and Rigol's Ultra Vision technology as well as a host of options.

2. EasyScope - Sciencetech 801C:



EasyScope - Sciencetech 801C is a New Trend. The Vertical Bandwidth is more than adequate for all our needs and we can easily view signals upto 40MHz.

3. NIELVIS Engineering Lab Workstation:



The NI Educational Laboratory Virtual Instrumentation Suite (NI ELVIS) II is a modular engineering educational laboratory solution developed specifically for academia.

4. Strain Gauge Trainer



Use of **strain gauge** for such a purpose can be studied using this **trainer**. This **Load cell trainer** is designed to measure the pressure of the cylinder by using a diaphragm as a primary transducer and **strain gauge** as a secondary transducer.

5. LCD Digital TVTrainer



This trainer has been designed with a view to provide theoretical and practical knowledge of a general LCD Digital TV (DTV) on SINGLE P.C.B.

6. DTH Trainer Kit



DTH trainer has been designed with a view to provide theoretical and practical knowledge of a Direct to Home Trainer (DTH) on Single P.C.B. Signals can be monitored and demonstrated at various testing point.

7. High-End Digital storage Oscilloscope (DSO)



ANNEXURE XV
CONSUMER ELECTRONICS LAB

High-End Digital storage Oscilloscope (DSO) can ensure the proper functioning of the device or design flaws allowing for a more intuitive visual diagnosis of the source of unexpected voltage.

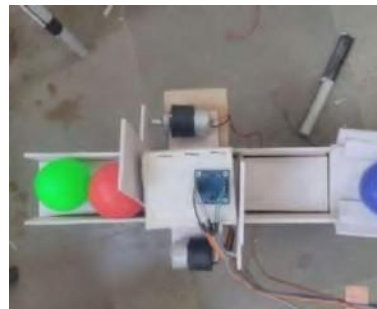
It allows probing of individual components and connections within electronic devices, acting as a simple signal tracer to determine the specific malfunctioning part besides providing alert regarding replacement need or fine tuning of electronic component.

Some of the Practical/Project Experiments:

1. Testing of Passive and Active components-
2. Characteristics diode: Transistor
3. Rectifying circuits
4. Filter circuits
5. Oscillator: Design of different type of biasing and their comparison
6. Study of Amplifiers
7. Verification of Thevenins, theorem for a two port network
8. Verification of Norton's theorem for a two port network
9. Maximum Power Transfer theorem
10. Series resonance - BW and Q factor
11. Parallel resonance -B.W. and Q-factor
12. To learn LCD Digital TV working and repairing using Trainer kit
13. To learn DTH working and repairing using Trainer Kit
14. To learn Dvd player working and repairing using Trainer Kit

PTH Mechanical assembly

Electro less plating is “plating without the use of electrical energy” a chemical reduction process which depends upon the catalytic reduction process of metal ions in an aqueous solution (containing a chemical reducing agent) and the subsequent deposition of the metal. Typical choice for irregularly shaped, highly detailed part shapes because of completely uniform deposit thickness and high precision. By using this process and principle, PTH process has been completed shapes because of completely uniform deposit thickness and high precision. By using this process and principle, PTH process has been completed.



ANNEXURE-XVI

INDUSTRIAL AUTOMATION LAB

Recent trend of merging control systems associated with both factory and process automation demands knowledge from diverse fields. The purpose of the lab work is to study automation of time critical systems that demand precise real-time readings and control.

Main Equipment's Available

1. NI 9217 4-Ch PT 100 RTD 24-bit, 100S/s/ch



The NI-9217 is compatible with 3- and 4-wire RTD measurements, and it automatically detects the type of RTD (3- or 4-wire) connected to the channel and configures each channel for the appropriate mode.

NI PCIe-6321, Xseries multifunction DAQ (16 AI, 24 DIO, and 2 AO), 250kS/s single channel sampling rate:



The PCIe-6321 offers analog I/O, digital I/O, and four 32-bit counters/timers for PWM, encoder, frequency, event counting, and more.

3. NI USB-6211 Bus-powered M series Multifunction DAQ device:



It offers analog I/O, digital input, digital output, and two 32-bit counters. The device provides an onboard amplifier designed for fast settling times at high scanning rates.

4. NI USB-9211A, 4Ch., 24-Bit Thermocouple input module:



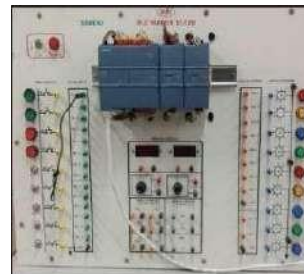
Plug-and-play connectivity via USB. Compatibility with J, K, R, S, T, N, E, and B thermocouple types. Small, portable device (12.1 x 8.6 x 2.5 cm).

5. Compact RIO:



Compact RIO (or cRIO) is a real-time embedded industrial controller made by National Instruments for industrial control systems. The **Compact RIO** is a combination of a real-time controller, reconfigurable IO Modules (**RIO**), FPGA module and an Ethernet expansion chassis.

6. Programmable Logic Controller

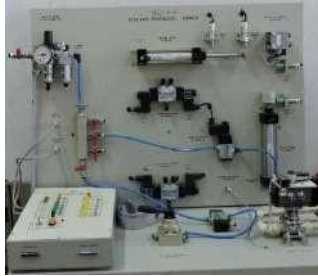


ANNEXURE-XVII

INDUSTRIAL AUTOMATION LAB

PLC is used for control applications as in special purpose machines for milling, drilling, packaging etc. PLC senses inputs from field (using its input cards), for example from a level sensor, a proximity switch, pushbutton etc. PLC's have been programmed in a language called as ladder language.

7. Electro-Pneumatic Trainer Kit



Electro-pneumatic control consists of electrical control systems and operating pneumatic power systems. In this solenoid valves are used as interface between the electrical and pneumatic systems. Devices like limit switches and proximity sensors are used as feedback elements.

8. Level Measurement



Capacitive Level Sensors also referred as Radio Frequency (RF) level sensors, are used for measuring process level at a specific point, multiple points or continuously over the entire vessel height. Level change results in a variation of capacitance value around the probe, depending upon the degree of immersion.



9. Siemens S7 300 & 1200 PLC, SCADA, HMI

Siemens S7 300 & 1200 PLC, SCADA, HMI is used for PLC programming on Siemens PLC software Simatic Manager & TIA portal V13 with Digital I/O as well as Analog I/O. SCADA designing with WinCC basic & advance Siemens SCADA software, HMI TP700 comfort programming & its applications.



10. Electro Hydraulic Trainer Kit

Electro Hydraulic Trainer outlines the basic Principle of Hydraulic Control System, Hydraulic Control System Components & its applications using electronic proximity position sensor & electro-mechanical actuators (solenoid valves).



Some of the practical project experiment:

1. Optimized Operation of Induction Generator for Small-scale Wind Power.
2. High Precision Stepper Motor Controller Implementation on FPGA with GUI on LabVIEW.
3. Real Time Data Monitoring of PV Solar Cell Using LabVIEW and DAQ.
4. Forest Fire Detection Using Optimized Solar-Power Zigbee Network
5. Optical Character Recognition Based Speech Synthesis System Using Lab VIEW
6. LabVIEW and Web-Server Based Human Body Monitoring System

ANNEXURE XVIII

Internet of Things

Objectives

The lab is equipped with popular boards such as ARM Cortex Processors with IDE of 10 users license, Raspberry Pi, Arduino and Node MCU ESP 8266 in addition to wi-fi, BLE and other connectivity modules. The campus is equipped with 24x7 wired and wireless internet connectivity.

Main Equipment's Available

1. Raspberry Pi3 B Board



Smart card size PC board with CPU of 4× ARM Cortex-A53, 1.2GHz, 10/100 Ethernet 802.11n wireless, Bluetooth, 40-pin GPIO header, 4× USB 2.0, Ethernet, Camera Serial Interface (CSI), Display Serial Interface(DSI)

2. Arduino Boards



Very cheap IoT platform with ATmega328P cpu, 6 analog inputs, 14 digital I/O pins include 6 PWM outputs.

3. Xbee modules with explorer and shield



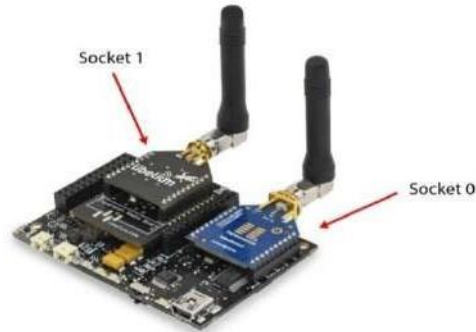
Low power, low cost modules for wireless mess networks suitable for home automation, environment monitoring etc.

4. SIM900 GPRS/GSM module



Very powerful single-chip board integrating AMR926EJ-S core, Quad - band GSM/GPRS module suitable for M2M solutions.

5. LoRa WAN module



Module provides Long range - wireless technology solution, low power and high capacity nodes.

6. Ethernet Shield



The Arduino Ethernet Shield allows an Arduino Board to connect to the internet. It is based on the (WiznetW5500 Ethernet chip).

Practical Project Experiments

1. Setting up of Raspberry Pi and connect to a network
2. Familiarization with GPIO pins and control hardware through GPIO pins.
3. Speed Control of motors using PWM with python programming.
4. Use sensors to measure temperature, humidity, light and distance.
5. Web based hardware control
6. Connect IOT devices through cloud using IoT protocol such as MQTT.
7. Controlling IoT devices using Arduino.
8. Create Wireless network of sensors using Zigbee.

ANNEXURE XIX
NETWORK and SERVER FACILITY

Objective

Server is CISCO 5108 with 4 B220 blade server each

All the classrooms and lab are well connected with central switching center and have 100Mbps NKN Link. For academic and research purpose there is also a facility of a mini Data center having CISCO UCS5108Chassiswith4NumbersofB220blade servers. For faulty tolerance and to decrease down time there are redundant Fabric Interconnect in clustered fashion.

Main Equipment's Available

1. CISCO Layer 3 Central Switch4507.



ThisCISCO L3 main switch deployed with two sup Engines, 2 Line card for 10G fiber optic ports and 2 Line card for 48 PoE Gigabit Ethernet copper ports with dual 6000w power supply. This form main 10G backbone on fiber optic connectivity for NIELIT Aurangabad.

2. CISCO Catalyst2960X-48TDL.



Every lab is deployed with CISCO Layer2 switch 48 Gigabit Ethernet port and 10G fiber optic port for uplink to main central L3 Switch.

3. UCS 5108 Chassis with 4 blade servers



having 2 Xeon processor and 192 GB RAM. Chassis is connected using Fabric Inter connect to 40 GB storage. The hardware is controlled is CISCO UCS and VMware software.

4. Storage IBM v3700



IBM storage is used to provide space to the VMs created on servers. Storage is configured and working on iSCSI connectivity with server Chassis.

5. CISCO router ISR 2911 and ISR2821



NIELIT Aurangabad is acting as CISCO network Academy for CCNA routing and switching course. The lab is equipped with 3 Nos. of 2911 CISCO routers to understand working and configure various routing protocols.

6. CISCO Catalyst switches2960:



There are 3 Nos. of CISCO Catalyst 2960 switches for CCNA practical. This is useful hands on practice to configure and troubleshoot various VLAN, VTP, STP, trunking protocols

ANNEXURE XX
NETWORK and SERVER FACILITY

1. High Speed WAN interface card

Cisco HWIC 2T serial port module is used to configure various WAN protocols like HDLC, PPP, Frame-relay, etc.



One server is being used as intranet server where various services are running like DNS, DHCP, Web, FTP, Telnet, etc. Authentication server is configured in Windows environment as Active Directory Server.

4. National Knowledge Network



The purpose of NKN goes to the very core of the country's quest for building quality

institutions.

The Centre is part of NKN and can seamlessly connect at gigabit speed and enables students, scientists, researchers to work together for accessing information to stimulate research and create next generation applications & services in critical and emerging areas.

2. Connection to NKN link



Whole office is using 100Mbps NKN link for public connectivity using Juniper route MX-8 and for security using Cyberoam 300iNG as firewall.

3. System Administration using RHEL Linux and Windows Server 2008



There are 3 Nos. of HP ProLiant DL160 Rackmount servers. One of those is deployed as Public web service where other is used as hardware accreditation portal is hosted.

5. Some of Practical Project Experiments

Students can practice and build their own network scenario and configure various protocols using the lab. Some of the practical experiment covered are:

1. Providing seamless, strong backbone connectivity to all labs.
2. Usage of High end Hardware and Software tools to deploy the infrastructure.
3. Creating of network scenario and configuration of routing protocols like Static routes, Dynamic routing protocols like RIP, OSPF, EIGRP and BGP, etc.
4. Experiments in the domain of networking particularly in routing and switching.
5. Students learn, practice and prepare for Industry Certification.
6. Project Experiments to get certified as CCNA.

ANNEXURE XXI

OPTO ELECTRONICS LAB

Objectives:

The Objective of the lab is to conduct Master level courses in Optical Fiber Communication, Test & Measurement of Optical Fiber parameters, project and Industry Oriented Short Term Courses of the field

Main Equipment's available

1. EXPO MAKE AX-110 ALL FIBER OTDR:

The OTDR EXPO AX-110 is ideal for occasional or experienced users looking for ruggedness, ease of use, automation and top-of-the-line optical performance. It performs testing at wavelenths (nm) 850/1310/1490/1550/1625 .



2. Connectorisation Kit



The Kit contain all necessary tools, consumables, connectors and cable to demonstrate and practice the process of Fiber preparation, Inserting the fiber into the connector, applying epoxy, cutting the fiber, & polishing the surface etc.

3. Core Allignment Fusion Splicing Machine:

A core alignment splicer (Sumitomo Electric Make) is a fusion splicer that uses the core alignment method to observe the core from two directions with a built-in microscope and determine through image processing the

position in which the central axes of the cores are aligned.



4. Transmitter/ Receiver (Model 310 And Model400):

BCP make Model 310 And Model 400 is high speed transmitter and receiver. This Model works at 850 and 1300nm and separate receivers are available with Si and Ge detectors.



5.6-1/2 Digit Precision Multimeter

The 6 1/2 digit precision Multimeter deliver a comprehensive range of functions to meet our most demanding measurements on the bench or in a system.



The digital multimeters performs the functions we would expect to see in a multifunction DMM, including

measuring volts, ohms, amps, and frequency measurements

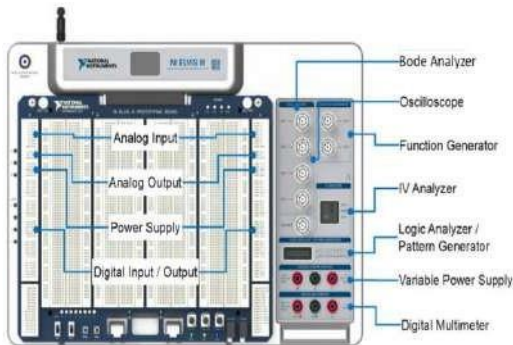
6. MIXED SIGNAL OSCILLOSCOPE (RHODE & SCHEWRTZ)

It has 4-Analog Channels & 16 Digital Channels with Bandwidth of 300 MHz, a sampling rate of 4GSa/s and high vertical sensitivity down to 1 mV/Div. The integrated three-digit digital voltmeter enables the users to simultaneously perform voltage measurements on all analog channels.



7. NI Educational Laboratory Virtual Instrumentation Suite (NI ELVIS)

NI ELVIS is a project-based learning solution that combines instrumentation, embedded



design, and web connectivity for engineering fundamentals and system design. It provides a comprehensive teaching solution for engaging students in hands-on labs involving analog circuits, mechatronics, power electronics, instrumentation, digital communications, digital electronics, controls, and more. Each laboratory solution includes lab material and complete experiments developed by experts in industry and education, so students can explore theory in the physical laboratory with a safe, in-depth experience.

8. Freescale MCU SLK

The Project Board consists of a large solder less breadboard area for building electronic circuits and has dedicated connectors for its Application Modules.



Some salient features of the Project Board are- on-board voltage regulators provide 4 different voltage levels (5V, 3.3V, +15V, -15V), push buttons, LEDs and LCD display for debugging, easy connectivity to the NI ELVIS/NI ELVIS II workstation via the NI ELVIS connector, power input from the included wall-plug transformer, integrated USB BDM, or from the NI ELVIS workstation.

Some of the Practical's/Experiments-

1. Handling and preparation of Optical Fibres.
2. Study and setting up an Optical fibre link
3. Study and Setting of an Digital Optical Link
4. Measurement of numerical aperture (NA) of an Optical fibre
5. Study of Losses in Optical fibres.
6. Voice and Data Transmission on Optical Fibres
7. Study of High speed Optical Links
8. Study of OTDR
9. Study of Fusion Splicing Machine
10. Preparation of Optical fibre connectors
11. Study of Intensity Based Sensors
12. Study of Network Analyser as Component Analyser.



ANNEXURE XXII

POWER ELECTRONICS LAB

Objectives

The lab aims at imparting practical knowledge of Power Electronics to the students at various levels i.e. DEPM, B.Tech and M.Tech. It is accordingly well equipped with equipment's and trainer kits to teach practical from fundamentals to high level concepts to the students.

Main Equipment's Available

1. Triac AC Phase Control



All Components are terminated with a connector for the study of Students. One potentiometer is provided to vary the firing angle of SCR. Another potentiometer is provided to vary the firing angle of TRIAC.

2. Single Phase Converter



Power circuit with a DC shunt motor 2SCRs/4SCRs and 2 power diodes. A circuit breaker, a bridge rectifier for field supply. 1 phase converter firing circuit. SCR converter with open/close loop with motor rpm indication, mech. Loading load regulation = 1% with tachometer F/B

3. SCR Lamp Flasher



Sciencetech PE40 SCR Lamp Flasher is compact, ready to use experiment board for lamp flasher using SCR circuit. This board is useful for students to study and understand operation of SCR controlled lamp flasher circuit and measurement of frequency, time, and voltage.

Some of the Practical project experiments.

1. Triac based speed control of small single Phase Induction Motor.

2. Electronic Heater Temperature control

3. Operation of SCR flasher.
4. Lamp brightness control using single Phase SCR Converter.

4. DC Chopper



DC Chopper Using SCR Trainer is very much useful for the students, to understand the principle of working and operation of the chopper. Output voltage can be controlled electronically by variation of Duty Cycle.

5. DC to AC Inverter



Power Electronic Training Board has been designed specifically for the study of working of inverter. A Battery 12V 80AH (Any car battery) is required to operate this apparatus. Different test points have been provided to check wave shape and amplitude of pulses how DC supply is changed to AC supply.

6. SCR Triggering Trainer



SCR trigger trainer system. UJT triggering circuit 24V 10W Lamp load 24 Volt ac supply for Circuit inputs LED indication for supply R-trigger circuit with phase angle control 5 degrees to 90 degrees R-trigger circuit half wave with phase angle control up to 180 degree maximum. UJT is an excellent triggering device which provides narrow gate pulses. Control is very accurate and from 0 Deg. to 180 Degree.

Practical project experiments.

1. DC Chopper based speed control of small DC Motor.
2. Lamp brightness control using inverter. Inverter driven small induction motor

ANNEXURE XXIII
PRINTED CIRCUIT BOARD (PCB) LAB

Objectives

Printed Circuit Board laboratory caters to the need of Electronic Designer. It brings out the importance of quality and reliability to Electronic Manufacturing Industries. The Printed Circuit Board are designed by considering DFM (Design for Manufacturing)/DFA(Design for Assembly) and DFT (Design for Testing). Students are getting real world experience in PCB design and Manufacturing processes involved in Electronic Manufacturing and assembly Techniques.

Main Equipment's available

1. CNC Machine:



CCD/ATC/XL is a high quality Computer Controlled Drilling machine with Automatic Tool Change (ATC). The ATC/XL CCDs allows directly processing of Excellon / Sieb& Meyer drill data or HP/GL route data for producing PCBs (drilling, cut-out-routing, isolation milling) or routing/ engraving plastics, aluminum and other metals. CCD/ATCXL has a larger work space (500x600 mm). The ATC is ideal for very big and complex PCBs with many different drill sizes and for special applications.

2. Electroplating Machine:

HitecPlate 3040 are universally applicable electroplating machines for the deposition of metals and serve for the production of plated-through-hole printed circuit boards in vertical technology for prototype and small batch production.



The HitecPlate 3040 is designed for direct metallization and have baths for process steps cleaning, rinsing, pre-dipping, activating, rinsing, intensifying, rinsing, copper deposition.

3. Etching / developing Machines:(DL 500)



The DL 500 is a double sided conveyorised spray etching machine with integrated rinsing zone. This machine is easy to maintain and fits perfectly to a modern PCB laboratory. The maximum capacity within one hour is 10 m². Designed for being used for laboratory purposes,

there are lots of different applications (e.g. spray developing of tenting or solder mask).

4. PCB Photo Plotter:

PCB Photo plotter (Film Master Plus XL from Bungard) is used for generation of high end film



artwork layout with maximum plot size of 360 mm X 430 mm and can process Gerber files or bit map files.

5. U.V.Exposure Unit:



This Unit is used to expose photo tool /Film master /Image on photosensitive coated board by exposing with proper wavelength, exposing time, light intensity, Temperature, type of photo tool. The light source affects the degree of polymerization of photopolymer.

7. Reflow Oven :

Reflow soldering is a process in which a solder paste (a sticky mixture of powdered solder and flux) is used to temporarily attach one or several electrical components to their contact pads, after which the entire assembly is subjected to controlled heat, which melts the solder, permanently connecting the joint.



7. SMD Components Pick And Place:



SMT (surface mount technology) component placement systems, commonly called pick- and place machines or P&Ps, are manual assisted or robotic machines which are used to place surface-mount devices (SMDs) onto a printed circuit board (PCB).

8. Stencil Printer :



Stencil printer is used to deposit solder paste on the Printed Circuit Boards (PCB's). The laser etched screen allows to dispense a set amount of solder paste required for soldering the component.

9. PCB Brushing Machine :



The PCB Brushing machine (RBM 300 from Bungard) is used for deoxidizing, light deburring and surface finishing of single & double sided PCBs with working width of 33 mm.

It is a new soldering station with a traditional heating system (ceramic heater), characterized by a 90 W power, temperature adjustment from 80°C to 480°C and temperature stability of $\pm 2^\circ\text{C}$.

The Quick 201B ESD Desoldering Tool allows removal of through hole soldered components with the minimum of effort, simply select the required temperature, place the nozzle over the lead and once the joint is molten press the trigger.

10. Hot Air Oven:



The Hot Air Oven (Scientech SE-127) is used for PCB baking process and has the temperature range of upto 300 degree centigrade with digital display for temperature settings.

11. Soldering & De-soldering Unit:

DSS36 is an ESD soldering station used for SMD components mounting and rework, soldering common and directly-inserted electronics and lead-free soldering.

Some of the Practical Experiments-

1. Introduction to Printed Circuit Board
2. Introduction to various Electronics component Footprints.
3. Design & Develop Single sided PCB Documents for Manufacturing
4. Design & Develop Double sided PCB Documents for Manufacturing
5. Introduction to Computer aided Design(CAD)
6. Introduction to various artwork generation method
7. Introduction to Etching Techniques.
8. Manufacturing of SSB
9. Manufacturing of DSB
10. Introduction to Soldering, soldering methods & component Assembly.



ANNEXUREXXIV
VLSI LAB

Objective

This Laboratory is well equipped for challenging IC design and validation. The Basys 3 FPGA development board design for Xilinx Vivado Design Suit featuring. The Artix-7 FPGA Architecture allows us put complex logic into device and verification logic. This supports for conducting the UG/PG labs and also research activities in M.S and PhD level.

Main Equipment's Available

1. Altera DE2-115 Development and Education Board



DE2-115 offers an optimal balance of low cost, low power and a rich supply of logic, memory and DSP capabilities

2. Basys-3 FPGA Board



Board has complete ready-to-use hardware, a large collection of on-board I/O devices, all required FPGA support circuits, and development tools.

3. Analog discovery 2 with parts kit



Digilent Analog Discovery 2 is a USB oscilloscope and multi-function instrument that allows users to measure, visualize, generate, record, and control mixed-signal circuits of all kinds.

Some of the Practical Project Experiments:

Digital circuits designing in Verilog and analog circuit design
Xilinx: VIVADO Simulation and Synthesis

Synopsys:

1. Design and Verify logic of all basic gates using Switch and Led.
2. Design all sequential circuits' logic.
3. Design all Combinations circuits Logic.
4. ALU Design
5. Decoder design UART design.

ANNEXURE XXV **EMBEDDED SYSTEM LAB**

Objectives

Studying a range of topics of immediate relevance to industry makes a student suitable for working in industries engaged in Embedded System and Electronic Product development. The purpose of this lab is to provide an excellent foundation for those wishing to engage in application research in this rapidly developing area.

Main Equipment's available

1. High-end multipurpose embedded Development board



This board is very useful for students to learn ARM7, AVR, 8051 and its interfacing with Micro SD Card interface, Graphical LCD, 8 LEDs, On board, LDR on board

2. ARM processor and Controllers: -



An ARM processor is one of a family of CPUs based on the RISC architecture developed by Advanced RISC Machines (ARM). The Keil MCBSTM32 Evaluation Board enables you to create and test working programs based on the STMicroelectronics STM32 family of ARM Cortex™-M3 processor-based devices.

3. AVR



Learn how to interface any sensor or input output device with ATmega32 microcontroller. Here we teach the students all input output interfacing.

4. 8085Microprocessor



The PS-8085 board which demonstrates the capabilities of the 40-pin 8085(various families). All programs are provided to demonstrate the unique features of supported device.

5. 8051Microcontroller



The P89V51RD2 are 80C51 microcontrollers with 16/32/64 kB flash and 1024 B of data RAM. The flash program memory supports both parallel programming and in serial ISP. Parallel programming mode offers gang-programming at high speed, reducing programming costs and time to market.

6. Arduino Development Board:



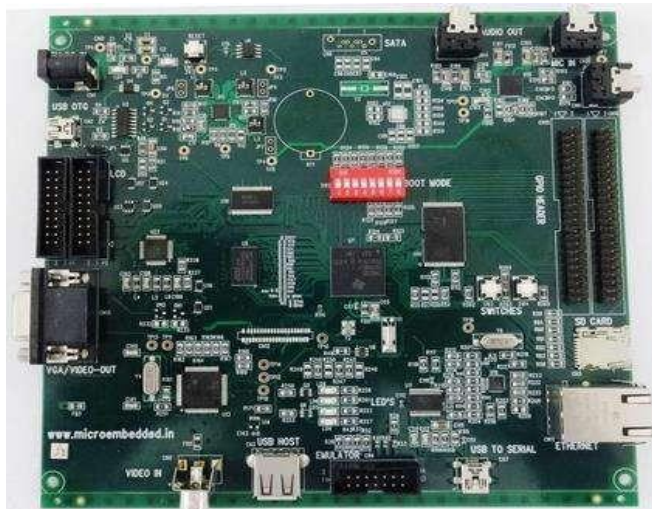
Arduino Development Board (micro embedded) used for programming of Micro ATmega328 & having flash memory of 32kb. Availability of interfacing present on microcontroller kit LED interfacing, Keyboard interfacing, LCD interfacing and other interfacing devices.

7. ARM CORTEX M3:

ARM CORTEX M3 (micro embedded) used for programming of LPC1768 & having flash memory of 512kb. Availability of interfacing present on microcontroller kit LED interfacing, Keyboard interfacing, LCD interfacing and other interfacing devices.

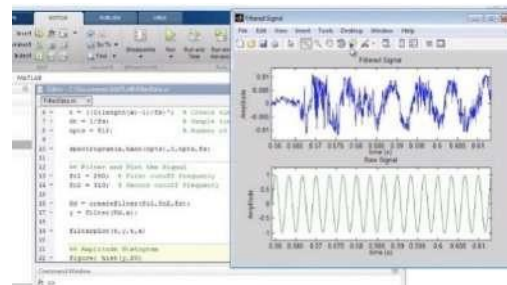


8. DSP Development Board:



The TMS320C6748 DSP development kit is a scalable platform that breaks down development barriers for applications that require embedded analytics and real-time signal processing, including biometric analytics, communications and audio. A wide variety of standard interfaces for connectivity and storage enable you to easily bring audio, video and other signals onto the board.

9. MATLAB-Math Works:



MATLAB combines a desktop environment tuned for iterative analysis and design processes with a programming language that expresses matrix and array mathematics directly. It includes the Live Editor for creating scripts that combine code, output, and formatted text in an executable notebook. MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages.

Some Practical Project Experiments

1. Familiarization with ARM board, RS-232C interface with PC
2. Traffic Light Controller
3. SPI interface, ADC interfacing
4. Dining Philosophers Problem implementation in ARM processor
5. RMS Scheduler using Free RTOS
6. Program to demonstrate I2C Interface on IDE environment
7. Study and observation of Position control of Servo Motor.

Some Innovative Project Works

- 1) An Android Controlled Mini Rover for real time surveillance using Raspberry Pi3
- 2) Smart Blind Stick



ANNEXURE XXVI

OPEN SOURCE COMPUTING LAB

Objective

Full-fledged three labs that are exclusively used for Open Source Software Development and Training purposes. All the labs are equipped with interconnected 10Gbps SFP+ port single mode fiber optics and has all PCs have at least i7 processor, 8 GB RAM, 100Mbps NKN Link etc.

Main Software's available

1. Operating System

The students have facilities to work and explore multiple Operating Systems viz **Ubuntu, Red Hat Linux, Fedora** and **Microsoft Windows -7,8,10.**

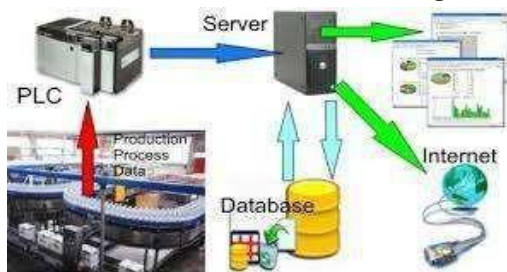
Course Available In Operating System:

- Certificate Course in System Administration using Unix
- Certificate Course in System Administration using Linux

2. RDBMS/ODBMS Software

Students can learn intricacies of Database Administration using both RDBMS and Object-Oriented DBMSs such as **MySQL, Oracle 11g** and **IBM DB2.**

Course Available In Database Management:



- Advanced Diploma in Big-Data Analytics
- Certified Data Scientist



3. Application Software's

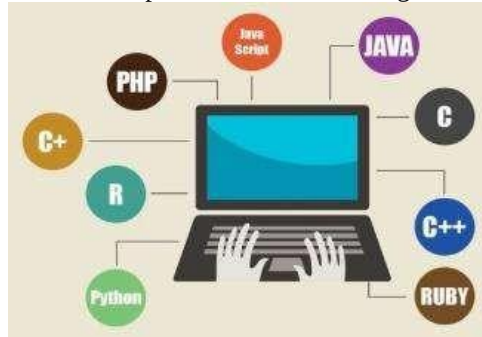
Students can master Web Application Development in all platforms of their choice such as Microsoft **DotNet, Java, LAMP.**

- Course Available In Application Software's and

Programming Languages :

Android Apps Developer

- Advanced Diploma in Java Enterprise
- Certified Course in Web Designing Course
- Certified Multimedia Developer
- Advance Diploma in .Net Technologies



4. Programming Languages

The Centre has licensed software to learn latest programming languages for development purposes such as SQL, Assembly, Python, Perl, PHP, Java, C/C++, C# and VB.Net

5. Mobile Application Development

Students are taught intricacies of professional Mobile Application Development on Android and Phone Gap platforms. Students can use latest tools for this purpose like Android Studio, AVD Manager, and Android Debugging Bridge etc..

6. Software for Scientific Use

Students can do simulation using Mat Lab and will do hands-on practical's on Industrial Grade Software such as AutoCAD, PLC & SCADA, Lab View, Xilieetc

7. Some Projects Developed by Students

9. 3-Tier Secured Web Application for Electricity Boards
10. Recruitment and Assessment Software
11. Digital marketing Mobile Application
12. Software for handling activities of Smart City Applications like Precision Agriculture, healthcare, Vehicular Traffic Management, Water Management, Home Automation etc.
13. Digital Image processing Software
14. Electrical Battery management System
15. Industrial Automation Software
16. Fault identification of Optical Fibre cable
17. Remote Control of Robots for Defense Application
18. Design of Industrial Grade device using CAD/CAM, 3D Printing and other Software's
19. Medicouse Mobile Application
20. Ultimate fruits Mobile Application

ANNEXURE XXVII

AR VR LAB

Objective:

The AR/VR Studio is a dedicated space for the exploration of cutting-edge immersive technologies physically and imaginatively. The AR/VR Studio is equipped with technologies that include various software and design platforms, multiple headset configurations, workstations, and filming resources to help students experiment and create innovations in a wide range of industries.

1. VR Headset: -



The HTC Vive is a virtual reality headset developed by HTC and Valve. The headset uses "room scale" tracking technology, allowing the user to move in 3D space and use motion-tracked handheld controllers to interact with the environment. The Vive headset has a refresh rate of 90 Hz and a 110 degree field of view. The device uses two OLED panels, one per eye, each having a display resolution of 1080×1200 (2160×1200 combined pixels). Safety features include a front-facing camera that allows the user to observe their surroundings without removing their headset. The software can also use the camera to identify any moving or static objects in a room; this functionality can be used as part of a "Chaperone" safety system, which will automatically display a virtual wall or a feed from the camera to safely guide users from obstacles or real-world walls.

2. Interactive Video Wall: -

QHR Series high brightness displays provide 4K UHD non-glare picture quality with up to a billion colors for crystal clear, lifelike color and exceptional detail even in bright ambient light. With an elegant, slim design and a clean cable management solution, these displays are easy to install into any type of environment. Plus, the embedded Magic INFO Player S6 software allows for easy content management and playback, without the need for an external PC. Slim, easy-to-install 4K UHD non-glare high brightness display delivering crystal clear, lifelike color plus intelligent UHD up scaling.

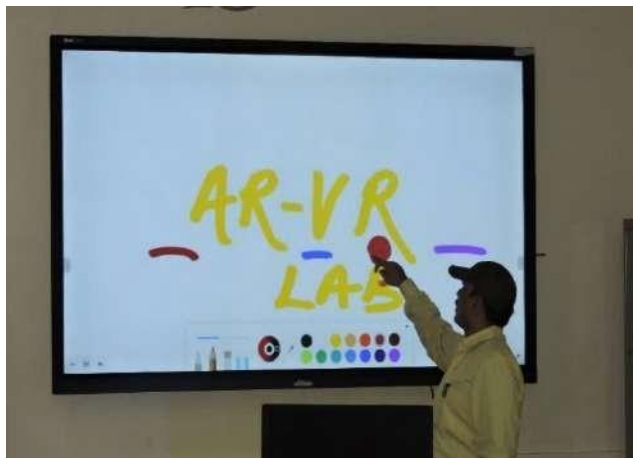


short, augmented reality has the potential to provide learners with contextualized information in real time to enhance learning experiences in the field. Augmented Reality has

approached a technical maturity where we can now say it is



Digital Workstation For Groups: -



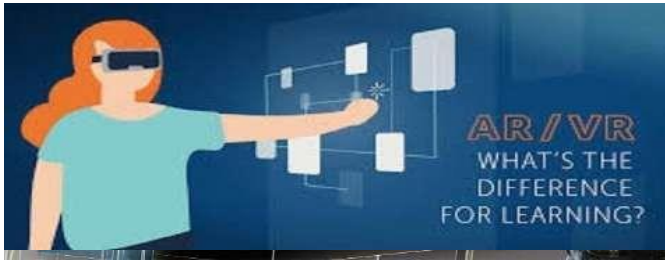
Augmented reality is the experience of actual environments that is supplemented by digital information in the form of images, sounds, and texts. Setup of HI-Tech. laboratory with state of art facilities and equipments such VR head Sets, Tablet computers & Digital Workstations and Interactive display etc. For example, the Star Walk app for smart phones and tablets allows users to identify over 20 000 objects in the night sky. Sky Walk takes advantage of the built in GPS and Internet connections to provide an interactive 360-degree touch control star map that displays constellations, stars, planets, satellites, and galaxies that are currently overhead.

Engineering labs have used augmented reality to conduct lab practices remotely and in interaction with an actual lab. In

7. AR's connections with Virtual Reality

present in our society and is now becoming a true presence in the modern world. AR will push innovation to new levels and create flexible and functional tools to help society how the public safety sector, military, and the legal field will be influenced by the use of Augmented Reality.

3. Artificial Intelligence



(AI)Artificial Intelligence is very much essential for the operations of augmented reality. AR allows objects to be labeled and identified in the viewer's visual point of view. Many social media applications that we are using today are developed with a combination of augmented reality and artificial intelligence. For example, on Instagram and Snapchat there are various fun filters like the dog filter, bunny ears, pig filter etc. which are based on consumer-facing applications. These applications do not function unless and until both AI and AR are combined and operated. We are sure going to witness and get to use a lot of more social media applications which have functionalities like image enhancements.

4. NIELIT AR in terms of Teaching and Training

Both the training and teaching technologies belong to the field of education. By using augmented reality technology, any information or data can be passed to a learner in real-time. These systems give a better sense of vision and pull out the objects and hazards which ultimately result in best-practice. Augmented Reality has proved its worth by decreasing the amount of risk and the cost rates in association with training.

Facebook is one of those most used social media applications which beliefs in augmented reality and virtual reality environments. AR vs VR is also a viral topic nowadays. Augmented Reality results in connecting people, socializing them with the help of virtual reality. Both virtual and augmented realities together worked in developing the" conference calls" where users can see one and other and at the same time, they can interact with each other. These conference calls can carry more than 2 people also at the same time. These tools and equipment allow users to witness pin boards and whiteboards. Augmented Reality along with virtual reality together work on design-based documents lay on real-time objects.



1. AR in the Automobile Industry

Autonomous cars may take a few more years' time to come into reality. Meanwhile, many automobile companies are making use of augmented reality and artificial intelligence technologies to mark their presence in the market. The automobile industry is an advent of augmented reality businesses. Automobile companies are currently working in dashboard-mounted display graphics from around a vehicle with the help of camera footage.

This technology is likely to reduce the occurrence of accidents, as it has the capability of pointing out towards hazards along with the identification of landmarks which are historical along the way. Seeing the results coming from AR technology-based companies, large-scale automobile companies like Tesla, Toyota, Mercedes-Benz, and Volvo have signed contracts to work on this platform. Augmented Reality technology also has the capability to take routes in protecting AR data onto a car's windshield.

With the help of augmented reality technology, we can also experience the identification of frequently used lanes, navigational prompts, detection of hazards, information about the right direction etc.



ANNEXURE XXVIII

LIBRARY INFRASTRUCTURE

Objectives

The library at NIELIT Aurangabad is housed in two floors of the library with a total area of 330 Sq.metre.

The library at NIELIT Aurangabad provides information services and access to information resources to support the training and research programs at the institute. It caters to the information needs of the students, researchers and scientists with its well managed information resources.



Library Resources:

The library has an extensive collection of resources in the form of books, journals, E-journals, theses and other documents covering major subject areas like Electronics, Computer Science, Embedded systems, Bioinformatics, Information Security, E-Learning, Agriculture, Bio technology, Control Engineering, Networking, Communication, etc

Books:-

There are around 15000 books in library which includes text books and reference books.

E-Resources:-

Access to Online MCIT-MeitY Library Consortium Resources is being provided to users. E-resources like Journals, transactions, and magazines, Conferences, IEEE published standards are made available to users through IEEE/IET Electronic Library (IEL). Full Text Collection of all ACM publication including journals, conference proceedings, technical magazines etc are made available to users through ACM Digital Library. It is accessible on any computer in the campus. Access to e-resources of National Digital Library available to students.

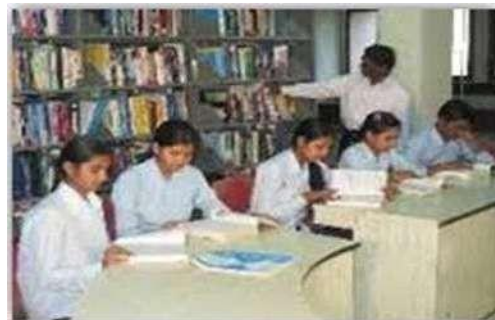
Journals, Magazines and Newspapers:-

Relevant Journals/Magazines are being subscribed in the library

Library also subscribes to leading newspapers in English, Hindi and Marathi available in the region

Facilities for students:-

Students can obtain the privilege to access library services after obtaining library membership. Reading room facility and reference services are provided to all the users of library. Students are provided with book borrowing facility and Library also provides book bank facility to B.Tech and Diploma students. They can access the E-journals and other E-resources through the institute network. Students can access freely the E-Resources from National Digital Library, IEEE/IET Electronic Library and ACM Digital library through institute network.



ANNEXURE XXIX

Multimedia Lab

Objectives:

The lab is equipped with state-of-art infrastructure and latest/popular software's to build proficiency in students in Multimedia and Animation to cater to the needs of growing Animation and Multimedia Industry. Students are enabled to apply knowledge, techniques, skills of modern multimedia tools in different digital media disciplines like text, images, audio, video and animation (2D & 3D).

Main Software's used

a. CorelDraw



Using Coreldraw practical training is imparted for Large format print designs, Billboards, Complete Branding and Mock-up design presentations, Cards/Letterheads/Brochures/Logo design, Vector based designs, Vinyl designs, Art work for laser/wood/metal cutting & engraving to name a few.

b. Adobe Photoshop



Using Adobe Photoshop image editing, manipulations and photo retouching are taught viz Integrities of creating, enhancing and editing of images, artwork, illustrations. Students are enabled to make Projects simulating a real-life painting or creating an alternative view of the universe. Tools are available for editing individual images as well as batches of photos & various video formats.

c. Adobe flash

Adobe flash for enabling professionals as well as beginners to create animations and interactive content for websites and applications.



d. 3D Max



3ds Max is a computer graphics program used for creating 3D models, animations, and digital images. The **software** can handle several stages of the animation pipeline including pre-visualization, layout, cameras, modeling, texturing, rigging, animation, VFX, lighting, and rendering.

e. Sound Forge

Developed and optimized by MAGIX this legendary audio



editor stands for innovation and combines the spirit of pioneering ambition with the art of engineering precision. Powerful editing, ultra-fast processing, crystal-clear audio quality and an innovative workflow are many other tools are available.

f. Adobe Premier Pro

Adobe Premier Pro is used for editing videos, commercials and other film, television, and online video. It allows its users to transform raw footage into incredibly amazing video



products. Variety of tools to fine-tune the audio, adjust colors, and do more to create professional-looking results are available

ANNEXURE XXX

Additive Manufacturing / 3D Printing Lab

Objectives

Additive Manufacturing / 3D Printing Lab is equipped with various configurations of 3D Printers, Professional 3D Scanner, also has flagship CAD/CAM software packages and high end CAD/CAM workstations to meet the present industrial requirements.

The lab is aimed at giving exposure and enhancing the knowledge and skills of engineers involved in the operation use of 3D Scanners, 3D Printers, CAD packages and for those who want to provide training to others in this area. It gives exposure and on hand experience in the field of Additive Manufacturing / 3D Printing, 3D scanning and, reverse engineering, Some of the facilities available as follows:

Main Equipment's Available

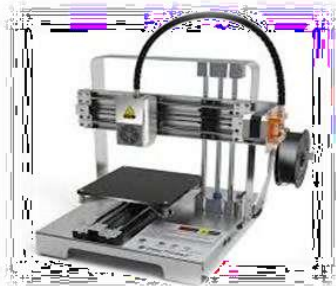
i. 3D Printer - Mojo



Mojo prints professional-quality models at your desk. It's as simple to use as a document printer, yet powered by FDM Technology to build spot-on, functional concept models and rapid prototypes in ABS plus thermoplastic. Mojo 3D Print Pack equipped with everything designers, engineers or educators need to start 3Dprinting. This 3D printer has an accessory of WaveWash 55 Cleaning System that dissolves and removes the support/waste material from the 3D printed objects to make printed parts ready to use..

1. 3D Printer Assemblies

This Lab also equipped with number of 3D printer assemblies. With the help of these kits students / participants will understand the working and assembling of 3D printers.



Students' versions allow to see the actual processing in the 3D Printer when it prints so that students can gain the knowledge of working of 3D Printers.

3. 3D Scanner

A metrological 3D solution (reverse engineering), perfect for capturing 3D objects for CAD applications and captured images will be transformed to 3D Computer Aided Design (CAD) models that helps in improving the designs without having CAD drawings for the existing products.



4. CAD/CAM Software

Major software packages available for this lab are

- Catia V5
- Creo (Pro/Engineer)
- MasterCAM
- AutoCAD



5. Some of the Practical Project Experiments

1. 3D printing the products of created models from CAD drawings
2. Scanning the parts to obtain 3DCAD models
3. Reverse engineering with the help of 3D Scanner
4. Assembling of 3D Printer kit.





Hostel and Canteen



Sports And Cultural Activities



Model Career Centre



NATIONAL INSTITUTE OF ELECTRONICS AND INFORMATION TECHNOLOGY, AURANGABAD
(Ministry of Electronics and Information Technology, Govt. of India)



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